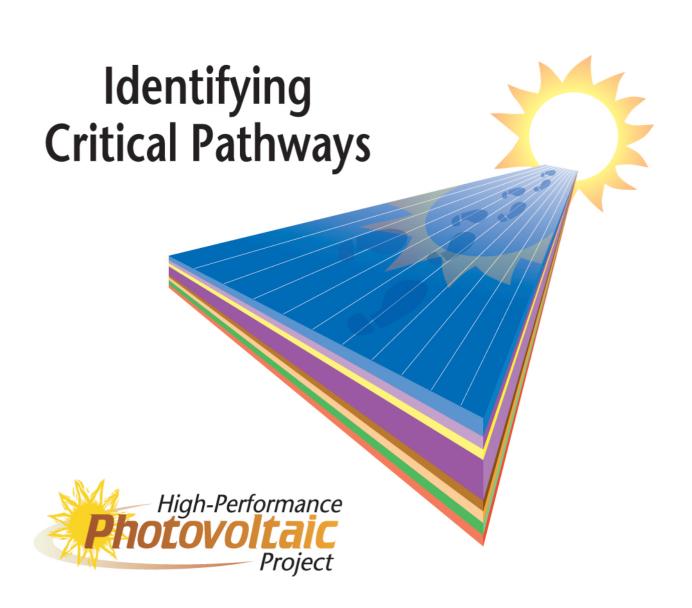
High-Performance Photovoltaic Project

Kickoff Meeting

NREL • October 18, 2001



High-Performance Photovoltaic Project

Kickoff Meeting October 18, 2001

Identifying Critical Pathways



National Renewable Energy Laboratory

1617 Cole Boulevard Golden, Colorado 80401-3393

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KICKOFF MEETING AGENDA

8:00-8:10	Martha Symko-Davies, National Renewable Energy Laboratory Welcome
8:10-8:25	Tim Coutts, National Renewable Energy Laboratory "Realistic Modeling of Thin-Film Tandem Solar Cells"
8:25-8:40	William Shafarman, University of Delaware "Wide Band Gap Cu(InGa)(SeS) ₂ and (CdZn)Te Thin Films for Tandem Solar Cells"
8:40-8:55	David Young, National Renewable Energy Laboratory "NCPV Thin-Film Tandem Research"
8:55-9:10	Akhlesh Gupta, University of Toledo "II-VI Tunnel Junctions and Absorber Alloys by Magnetron Sputtering"
9:10-9:25	Ingrid Eisgruber, Global Solar "Progress Toward 20% Efficient CuIn _x Ga _{1-x} Se ₂ Photovoltaic Devices on Foil Substrates"
9:25-9:40	Doug Rose, First Solar LLC "High Performance PV Tasks at First Solar"
9:40-9:55	Chris Ferekides, University of South Florida "II-VI Based High Band Gap Devices for Tandem Applications"
9:55-10:10	Oscar Crisalle, University of Florida "CGS/CIGS Tandem Cells: Critical Manufacturing Issues"
10:10-10:25	Break
10:25-10:40	Angus Rockett, University of Illinois "CuInSe ₂ Heterojunctions and Heterojunction Solar Cells With GaAs and Ge"
10:40-10:55	Michael Mauk, AstroPower, Inc. "InGaP/GaAs-on-Ceramic Thin-Film Monolithically Interconnected, Large Area, Tandem Solar Cell Array"
10:55-11:10	Sarah Kurtz, National Renewable Energy Laboratory "NREL Basic Research Toward a 40% Efficiency"
11:10-11:25	Keith Emery, National Renewable Energy Laboratory "Reference Conditions for PV Concentrators"

11:25-11:40 Raed Shariff, Spectrolab, Inc.
"High Performance, Low Cost III-V PV Concentrator Module"

11:40-11:55 Mark O'Neil, ENTECH, Inc.
"Development of Terrestrial Concentrator Modules Using High-Efficiency Multi-Junction Solar Cells"

11:55-12:10 Mark Stan, Emcore
"A Three-Junction Solar Cell for High Concentration Applications".

12:10-12:30 Break

12:30-1:30 Discussion

Topics:

- 1) Sharing/coordinating of information
- 2) Website
- 3) Logistics Meeting, when, where, and how often
- 4) Report at end of Phase I
- 5) Nonstructured group meetings inclusive for next phase??

High-Performance Photovoltaic Project

Kickoff Meeting

Identifying Critical Pathways

Martha Symko-Davies

National Center for Photovoltaics • National Renewable Energy Laboratory
October 18, 2001





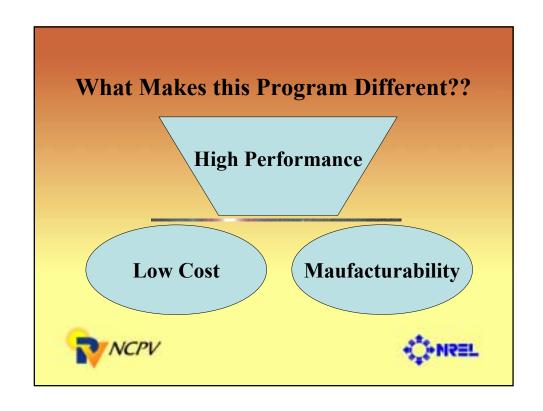
Near Term Key Targets	Date
Demonstrate a 20% Efficiency Thin Film Cell under Low Concentration	2001
Identify Key Issues and Pathways for Achieving a 33% Concentrator Module and a 25% Thin Film Multijunction Cell	2002
Demonstrate a 34% Cell under Concentration	2003
Full Implementation of Thin Film Process Integration	2004

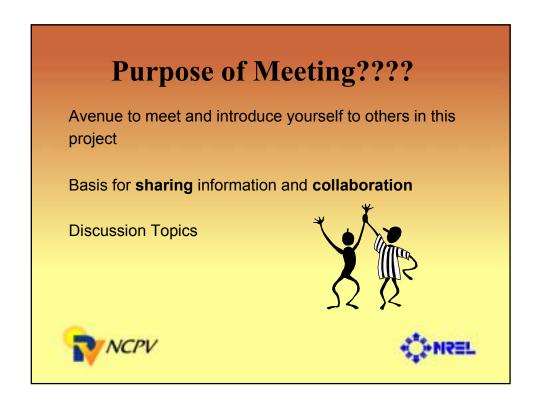
Demonstrate
Polycrystalline Dual-Junction
Thin Film Cell of 25% Efficiency

Demonstrate a 40% Cell under Concentration











8	erformance PV subcontractors
	Polycrystalline
Subcontractor	Title
*Astropower	InGaP/GaAs-on Ceramic Thin-Film Monolithically Interconnected, Large Area, Tandem Solar Cell Array
University of Delaware	Thin Film Multijunction Solar Cells: Development of a High Bandgap Cell (CIS and CdTe alloys)
University of Florida	Identification of Critical Paths in the Manufacturing of Low-Cost High-Efficiency CGS/CIS Two-Junction Tandem Cells (Single- Crystal CIS-alloy top cells on GaAs)
University of Toledo	Polycrystalline Thin-Film Tandem Photovoltaic Cells (CdTealloys)
University of South Florida	Development of a II-VI-Based High Performance, High Band Gap Device for Thin-Film Tandem Solar Cells(CdSe and CdTe-alloy)
Global Solar	Progresss Toward 20% Efficient CuIn _x Ga _{1-x} Se ₂ Photovoltaic Devices on Foil Substrates

High Performance PV subcontractors Concentrators

Subcontractor	Title
Univ. of Illinois	Cu(In,Ga)Se ₂ Heterojunction Solar Cells for Extreme High- efficiency Photovoltaic Concentrators (CIS on single-crystal GaAs for potential use in III-V multijunctions for concentrators)
Entech, Inc.	Near-Term Integration of III-V Cells Operating at 440X, Into Entech's Field Proyen Concentrator Module
SunPower Corporation	Lens-Based Concentrator Modules: Exploring Critical Optical and System Integration Issues
Spectrolab, Inc.	High Efficiency, Low Cost, IIII-V Concentrator PV Cell & Receiver Module
Emcore	A Three-Junction Solar Cell for High Concentration Applications (III-V multijunctions-lattice mismatched structure)





DiscussionTopics

- 1) Purpose of Meetings
- 2) Structure: Working Groups???
- 3) Meeting Logistics -when, where, and how often
- 4) Report at end of Phase I
- 5) Inclusive for next phase





Realistic modeling of thin-film, tandem solar cells

Timothy J. Coutts, J. Scott Ward,
David L. Young, Timothy A. Gessert, and
Rommel Noufi
tim_coutts@nrel.gov

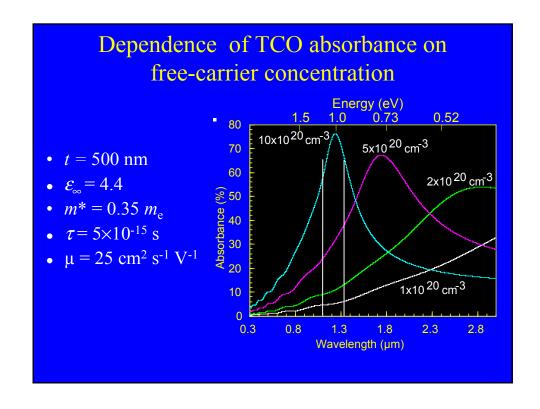


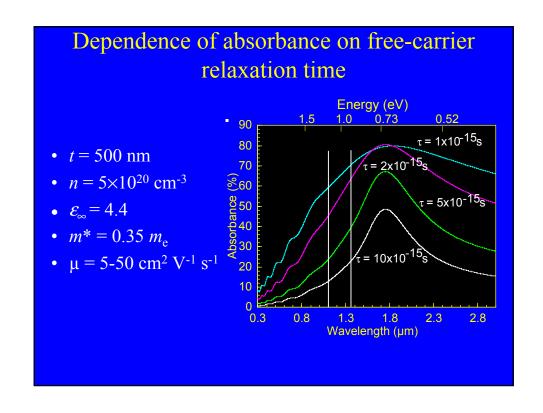
*NREL



Issues

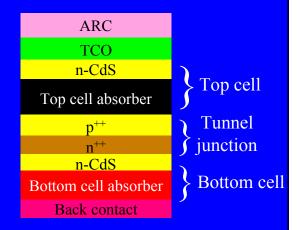
- A transparent conducting oxide (TCO) is used in all thin-film solar cells
- Free-electrons absorb in the near-infrared portion of the spectrum
- Depending on their concentration and mobility, they can impact cell performance
- In tandem cells their effect can be large





Schematic of modeled thin-film tandem cell

- All interfaces assumed to be specular
- No interdiffusion
- Top p-type absorber assumed to be chalcopyrite



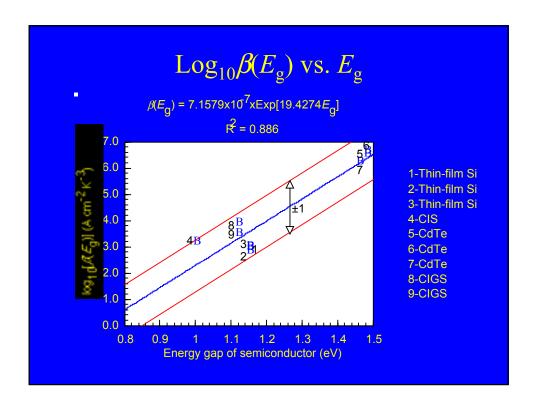
Assumptions and modeling approach

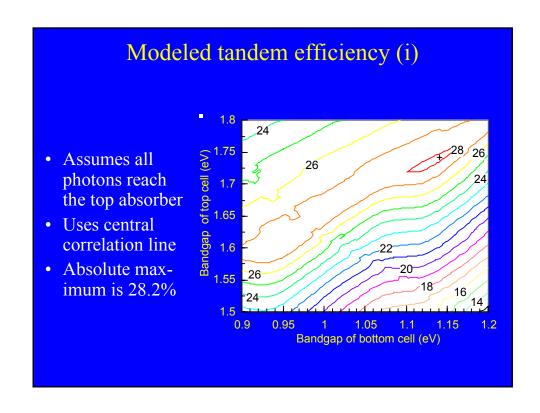
- Calculate light transmitted into the top (chalcopyrite) absorber, and, hence, J_{sc1} (Global reference spectrum)
- Assume all photons with $E_{\rm g1} < E < E_{\rm g2}$ contribute to $J_{\rm sc2}$
- Put J_{sc} equal to the smaller of \overline{J}_{sc1} and \overline{J}_{sc2}
- Take ideality factor as 1.5
- Calculate J_0 from a quasi-empirical model
- Calculate $V_1(J)$, $V_2(J)$ and $V_T(J)$
- Calculate fill-factor, $P_{\rm mpp}$ and efficiency as ${\rm f}(E_{\rm g1},E_{\rm g2})$

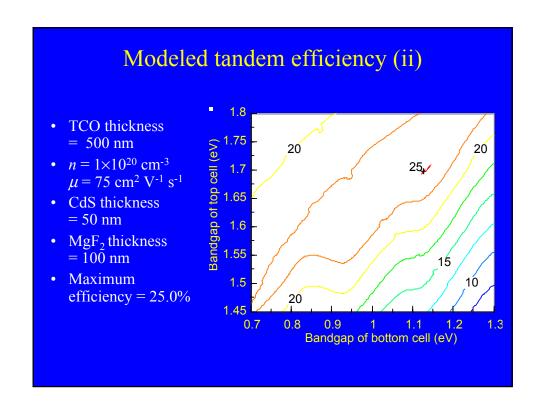
Modeling of J_0

$$J_0(E_g) = \beta(E_g)T^3 \exp(-E_g/kT)$$

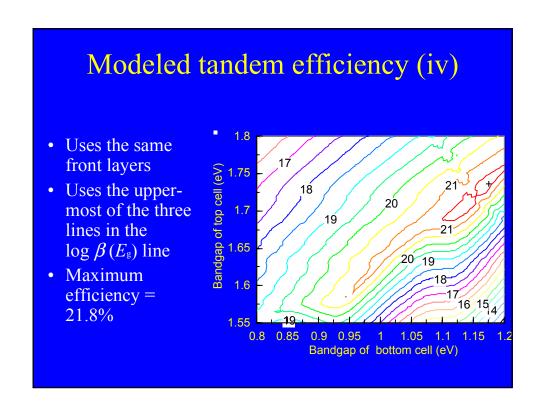
- Calculate $\beta(E_g)$ and correlate with E_g for the best single-junction thin-film devices
- This gives a reasonable estimate of $J_0(E_g)$ for devices of uninvestigated bandgaps
- It does **not** define the absolute limit





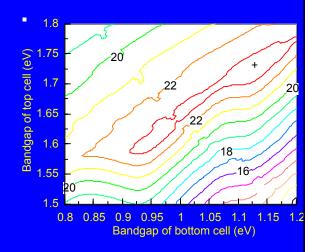


• Uses the same front layers • Uses the lowest of the three lines in the $\log \beta(E_8)$ correlation • Maximum efficiency = 28.7%



Modeled tandem efficiency (v)

- Uses a TCO with $n = 5 \times 10^{20} \text{ cm}^{-3}$, $\mu = 75 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ t = 500 nm
- Uses central $\log \beta(E_g)$ line
- Maximum efficiency = 23.6%



Summary

- TCOs with low carrier concentration and high mobility needed to minimize free-carrier absorption
- Beneficial to eliminate the TCO and the CdS
- Depending on J_0 , the optimum bandgap pair is about 1.7 and 1.13 eV
- The goal of 25% is achievable but challenging!
- Reduction in J_0 would give wider choice of materials and a higher potential maximum efficiency

What are the dominant factors?

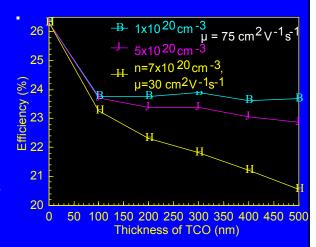
- The TCO film's optical properties are governed by Maxwell's equations
- The electron motion is described by a second order differential equation
- This was the approach used by Drude over 100 years ago

Goals of High Performance Project

- 40% laboratory demonstration of singlecrystal device
 - probably monolithically grown
- 25% laboratory demonstration of thin-film polycrystalline device
 - monolithic or mechanically stacked device

Dependence of efficiency on TCO thickness

- Always higher with lower carrier concentration
- Both high mobility materials
- Differences will increase as mobility decreases



Modeling of $J_{\rm sc}$ • All TCO thicknesses are 500 nm 1x10²⁰cm⁻³ • $\mu = 75 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1} \text{ for}$ 5x10²⁰cm⁻³ n = 1 and 5×10^{20} cm⁻³ 7x10²⁰cm⁻³ • $\mu = 30 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1} \text{ for}$ $\mu = 30 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ $n = 7 \times 10^{20} \text{ cm}^{-3}$ E_q of bottom c • All use 50 nm CdS and 100 nm MgF₂ 0.3 8.0 1.3 1.8 2.3 Wavelength (µm)

Everything depends on $J_0!$

Wide Band Gap CuInSe₂- and CdTe- based Thin Films for Tandem Solar Cells

Bill Shafarman Brian McCandless Mario Gossla

Institute of Energy Conversion University of Delaware



High Performance PV October 18, 2001

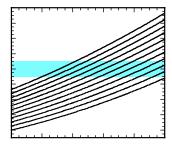
$CuIn_{1-x}Ga_x(Se_{1-y}S_y)_2$ Wide Band Gap Cells

 $CuIn_{1-x}Ga_x(Se_{1-y}S_y)_2$

- \blacktriangleright Increase $E_{\rm g}$ with minimum. cation and anion alloy concentrations.
- Admittance spectroscopy lower defect concentrations than CuIn_xGa_{1-x}Se₂ or CuIn(Se_{1-y}S_y)₂ films with comparable E_g Friedlmeier and Schock, 2nd World Conf. PVSEC, 1117 (1997).

Deposition approaches

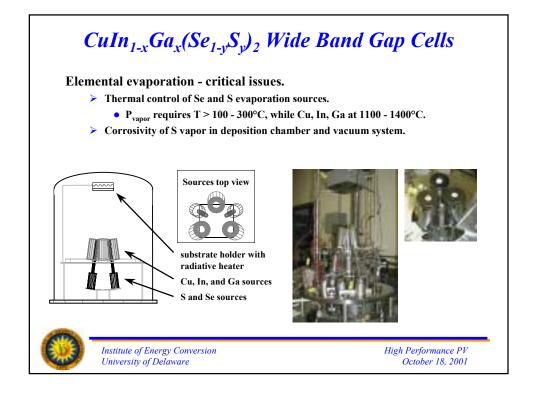
- Se + S reaction of Cu/Ga/In precursors
 - Ga ends up at back of film
 - Requires T≈ 600°C anneal to form uniform bandgap Marudachalam,et. al. Appl. Phys. Lett 67, 3978 (1995).





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$CuIn_{1-x}Ga_x(Se_{1-y}S_y)_2$ Wide Band Gap Cells ➤ S replacement reaction: $CuIn_{1-x}Ga_xSe_2 + H_2S \Rightarrow CuIn_{1-x}Ga_x(Se_{1-y}S_y)_2$. • Films reacted in combinations of flowing $H_2S + Ar + O_2$ at 1 atm. • Time to uniformly incorporate S into CuInSe2: ♦ ~ 1 hour for Cu-rich films. Engelmann, et. al., Thin Sol. Films 387, 14 (2001). ♦ >> 8 hours for Cu-poor (device quality) films. • Ga moves to back of film (toward Mo contact). 50 40 Atomic Conc (%) Atomic Conc (%) reaction in H₂S 180 min, 525°C 20 40 Sputter Time (min.) Sputter Time (min.) Institute of Energy Conversion High Performance PV University of Delaware October 18, 2001



CdTe-based Wide Band Gap Cells

Material selection criteria:

- Complete miscibility
- > Isostructural, isoelectronic p-type
- ➤ Chemical stability (e.g., HgTe unstable at T>400°C)
- > minimize perturbation from CdTe

Cd_{1-x}Zn_xTe

Compound	E _g Range (eV)	End-Point Structure	Miscibility Gap ?		
	Cation Substitution				
Cd _{1-x} Zn _x Te	1.49 - 2.25	ZB - ZB	N		
Hg _{1-x} Cd _x Se	0.10 - 1.73	ZB - W	N		
Hg _{1-x} Zn _x Te	0.15 - 2.25	ZB - ZB	N		
	Anion Substitution				
CdTe _{1-x} S _x	1.49 - 2.42	ZB - W	Y		
$CdTe_{1-x}Se_x$	1.49 - 1.73	ZB - W	N		
$CdSe_{1-x}S_x$	1.73 - 2.42	W - W	N		
$HgTe_{1-x}S_x$	0.15 - 2.00	ZB - ZB	?		
$HgSe_{1-x}S_x$	0.10 - 2.00	ZB - ZB	N		

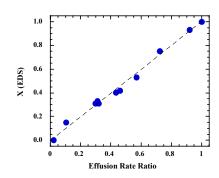


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Cd_{1-x}Zn_xTe Wide Band Gap Cells

Deposition:

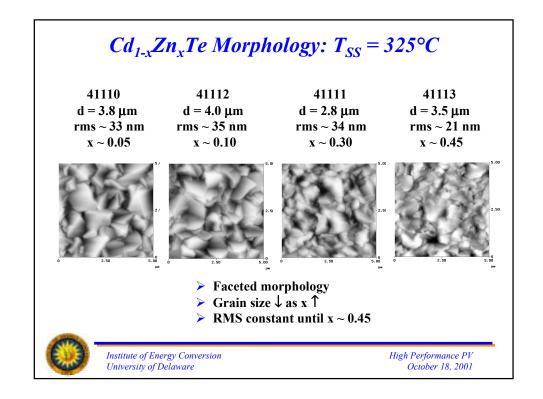
- Evaporation from CdTe and ZnTe compounds
 - Control film composition by relative effusion rate ratio r_{ZnTe}/(r_{CdTe} + r_{ZnTe})
 - High sticking coefficient
- $T_{SS} = 325$ °C
- glass/ITO/CdS substrates





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$Cd_{1-x}Zn_xTe \ Wide \ Band \ Gap \ Cells$ $Cd_{1-x}Zn_xTe \ films \ have been deposited over entire composition range.$ $> \ Optical \ band \ gap \ 1.5 \le '' \ E_g \le '' \ 2.55 \ eV \ and \ bowing \ parameter \ b = 0.3.$ $> \ Single \ phase \ films \ with \ linear \ variation \ in \ lattice \ parameter \ a_o \ determined \ from \ x-ray \ diffraction.}$ $> \ Sub-E_g \ optical \ transmission = 70 - 90\% \ over \ target \ band \ gap \ range.$



Transparent back contact for CuIn_{1-x}Ga_xSe₂

Requirements for contact include:

- > chemical stability in Se atmosphere
- ohmic contact
- > adhesion and structural stability

Evaluate available TCO materials

- ➤ Calculate equilibrium stability in Se₂ at 400°C
- ZnO actually ZnO:Al₂O₃(2%)
- ➤ ITO actually In₂O₃:SnO₂(9%)

reaction, at 400°C	∆G _{rxn} (kcal/mol)
$2ZnO + Se_2 \rightarrow 2ZnSe + O_2$	104
$2Al_2O_3 + 3Se_2 \rightarrow 2Al_2Se_3 + 3O_2$	416
$2In_2O_3 + 3Se_2 \rightarrow 2In_2Se_3 + 3O_2$	172
$SnO_2 + Se_2 \rightarrow SnSe_2 + O_2$	74

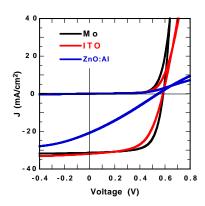


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Transparent back contact for Cu(InGa)Se₂

Deposit Cu(InGa)Se2 on Mo, ITO, and ZnO

- ➤ In a single Cu(InGa)Se₂ run
- all contacts deposited on standard soda lime glass
- $T_{SS} = 400$ °C
- ightharpoonup Ga/(In+Ga) = 0.32, E_g = 1.20eV
- no apparent difference in composition, morphology



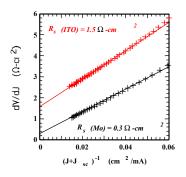


Institute of Energy Conversion University of Delaware High Performance PV October 18, 2001

Transparent back contact for Cu(InGa)Se₂

contact	Voc	Jsc	FF	eff	Roc
	(V)	(mA/cm ²)	(%)	(%)	$(\check{\mathbf{z}} - \mathbf{cm}^2)$
Mo	0.587	31.4	67.7	12.5	2.0
ITO	0.584	32.0	58.4	10.9	3.7
ZnO	0.544	20.7	27.7	3.1	23

- ➤ ITO/Cu(InGa)Se₂ cell comparable to Mo/Cu(InGa)Se₂ except series resistance
- > as-deposited
 - $R_{\text{sheet}}(M_0) \approx 0.2 \Omega/\Box$
 - $R_{\text{sheet}}(ITO) \approx 20 \ \Omega/\Box$





Institute of Energy Conversion University of Delaware High Performance PV October 18, 2001

Future research focus

 $CuIn_{1-x}Ga_x(Se_{1-y}S_y)_2$

- > Verify control of chalcogen sources. Calibrate all sources.
- > Develop process to deposit films with uniform composition. Characterize films, devices.

Cd_{1-x-}Zn_xTe

- Determine effects of post-deposition treatments in halide, oxygen and inert ambient at 300 - 500°C.
- > Fabricate and characterize devices.



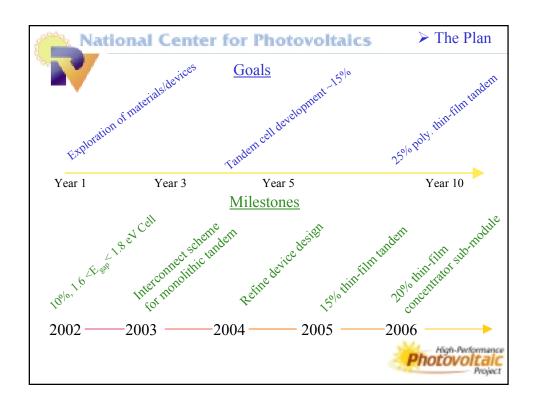
High Performance PV October 18, 2001



NCPV Thin-Film Tandem Research

D.L. Young, T.J. Coutts, T. Gessert, S. Ward, R. Noufi, S. Asher, K. Emery, D. Levi, H. Moutinho, Y. Yan, P. Sheldon, M. Symko-Davies





National Center for Photovoltaics Perspective

Address at Rice University on the Nation's Space Effort President John F. Kennedy Houston, Texas September 12, 1962

"We set sail on this new sea because there is new knowledge to be gained, and new rights to be won, and they must be won and used for the progress of all people."

"We choose to go to the moon. We choose to go to the moon in this decade and do the other things, not because they are easy, but because they are hard, because that goal will serve to organize and measure the best of our energies and skills, because that challenge is one that we are willing to accept, one we are unwilling to postpone, and one which we intend to win, and the others, too."

"But if I were to say, my fellow citizens, that we shall send to the moon, 240,000 miles away ...a giant rocket more than 300 feet tall,... made of new metal alloys, some of which have not yet been invented, capable of standing heat and stresses several times more than have ever been experienced, fitted together with a precision better than the finest watch, carrying all the equipment needed for propulsion, guidance, control, communications, food and survival, on an untried mission, to an unknown celestial body, and then return it safely to earth,... and do all this, and do it right, and do it first before this decade is out--then we must be bold."



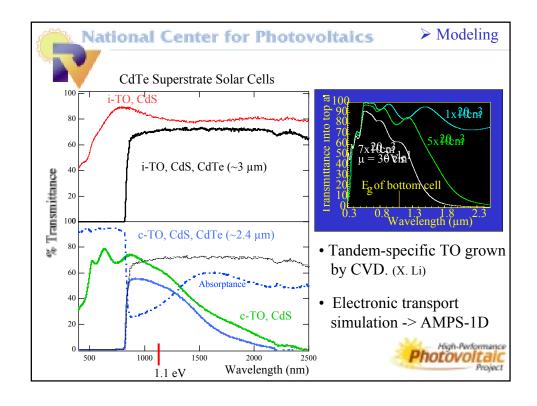
National Center for Photovoltaics

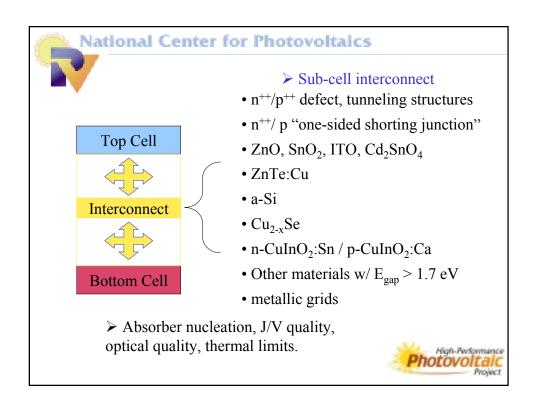
➤ 2002 tasks

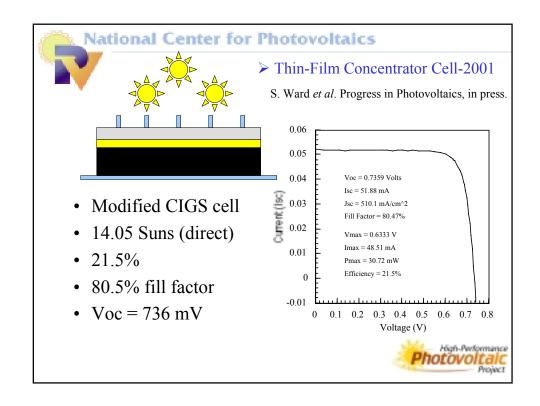
NCPV Thin-Film Tandem Research - 2002 A.O.P.

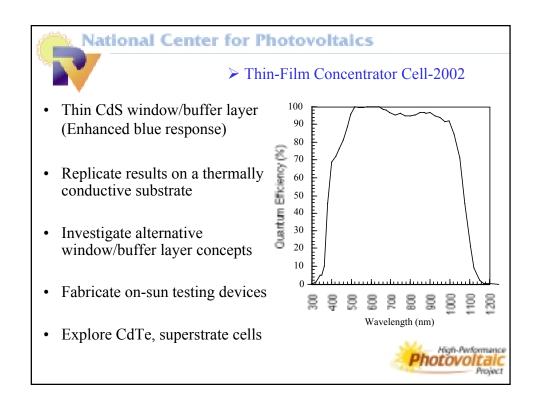
- Modeling
- Thin film concentrator cells
- Sub-cell interconnect
- Exploration of wide bandgap top cell materials
- New equipment and personnel

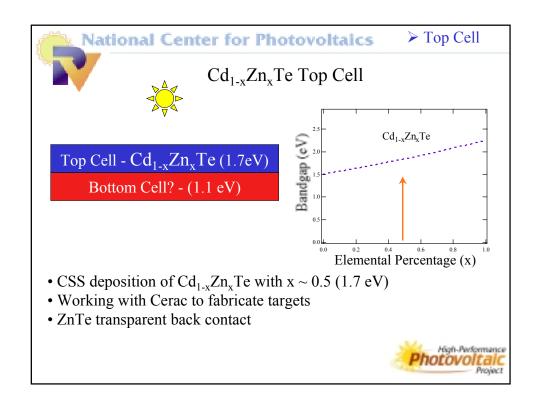


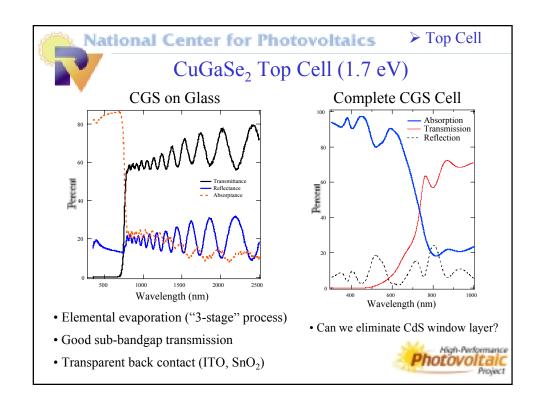


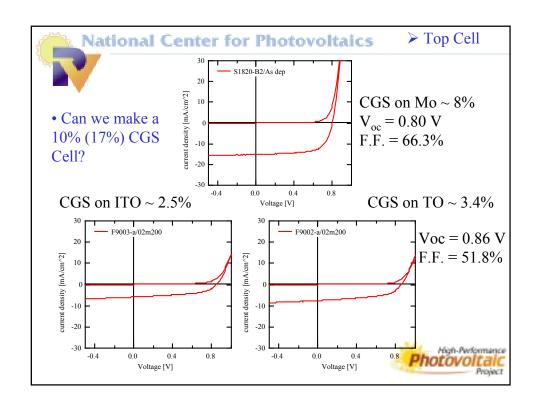


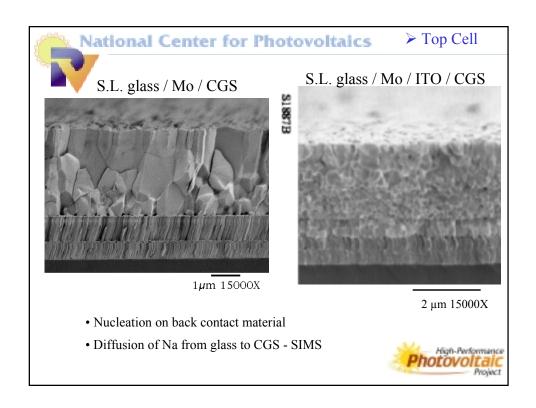


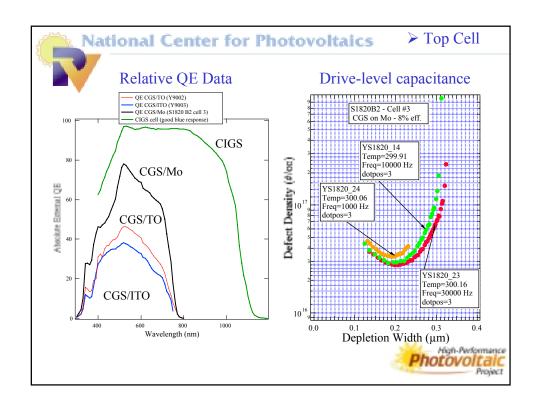














- > Equipment and Personnel
- Retool existing Solar-X evaporator
 - 5 sources
 - Active substrate cooling
- Recruit in-house specialist to work on High Performance goals.
 - ~1/3 of NREL work Future PV

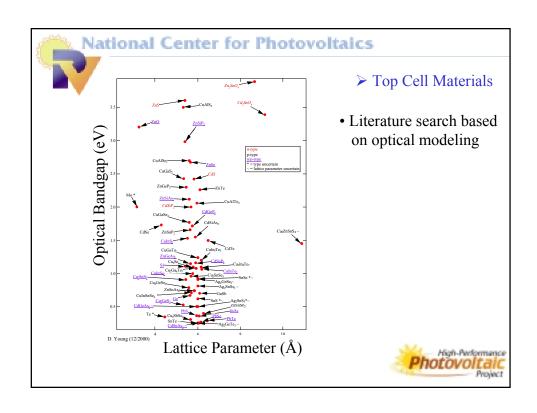


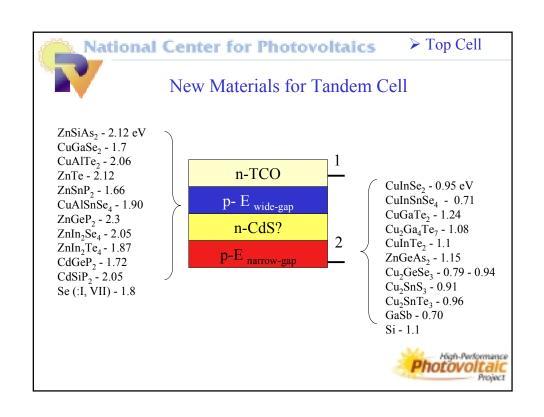


"And, therefore, as we set sail we ask God's blessing on the most hazardous and dangerous and greatest adventure on which man has ever embarked."

- J.F. Kennedy 1962







High Performance PV Research at U. of Toledo: "Polycrystalline Thin-Film Tandem PV Cells"

- First Solar, LLC, is a major lower-tier subcontractor
- University of Toledo focus:

First Year --

- magnetron sputtering of wide gap II-VI alloys for top cells with emphasis on CdZnTe
- magnetron sputtering of ZnTe:N and ZnO:Al for top cell transparent back contacts and recombination junctions
- (with FS) study of HRT layers for reduced CdS thickness
- (with FS) fabrication of four-terminal stacked device with CIS bottom cell

Second year--

- optimization of CdZnTe or other II-VI with special focus on post-deposition chloride heat treatment
- optimization of recombination junctions between wide-gap II-VI and CIS
- exploratory work on magnetron sputtered HgCdTe for bottom cells with possible fabrication of two-terminal structures with MS and/or VTD top cells
- characterization of materials-- PL, EL, Raman, XRD, Hall, photo-Hall, SEM/EDS
- device characterization including QEs of component cells of two-terminal structures

Absorber Layer, Cd_{1-x}Zn_xTe

Top Cell Absorber Layer Band Gap ~ 1.7-1.85 eV

CdTe Band Gap ~ 1.5 eV

ZnTe Band Gap ~ 2.2 eV

Preparation of Cd1-xZnxTe Films: By Magnetron Sputtering

 $\underline{\textbf{Target}}$: in house target fabrication with mixture & sintered $Cd_{1-x}Zn_{x}Te$

Challenge: Post deposition treatment

Transparent Recombination Contact

ZnTe:N - Band Gap $\sim 2.2 \text{ eV}$

- Prepared by Reactive Sputtering Technique

- N₂ Composition: 0 - 5% in Ar

Effect of N doping

Structural: orientation changes from (111) to (220)

Morphology: grain size decreases from 130 to 36 nm

Energy Band Gap: No trend, between 2.16 to 2.22 eV

Properties of ZnTe:N Films (3% N₂)

Resistivity: ~ 8 ohm-cm (as deposited)

: ~ 3 ohm-cm (heat treated in air)

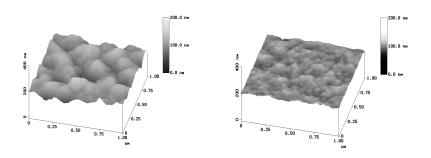
Carrier concentration: $\sim 5x10^{18} \text{ cm}^{-3}$

Mobility: $\sim 0.3 \text{ cm}^2/\text{V-s}$

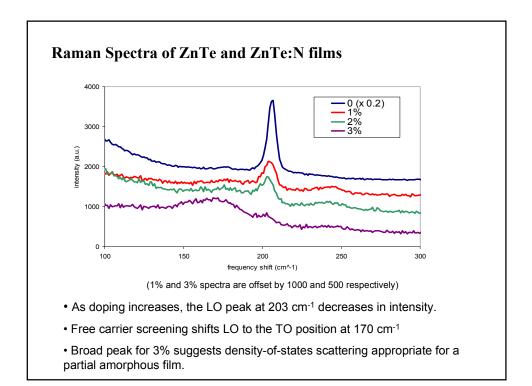
ZnO:Al Films: By Magnetron Sputtering Technique

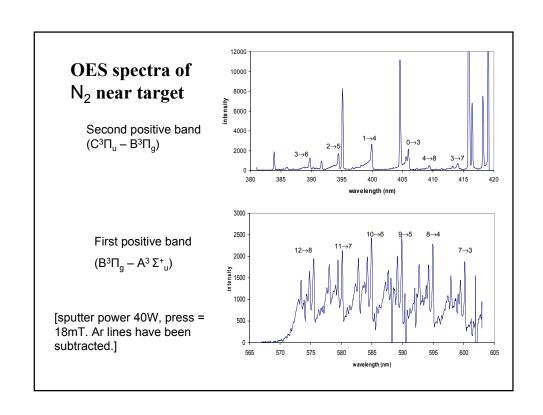
Window Layer: Use of High-resistive Tin Oxide to decrease CdS thickness

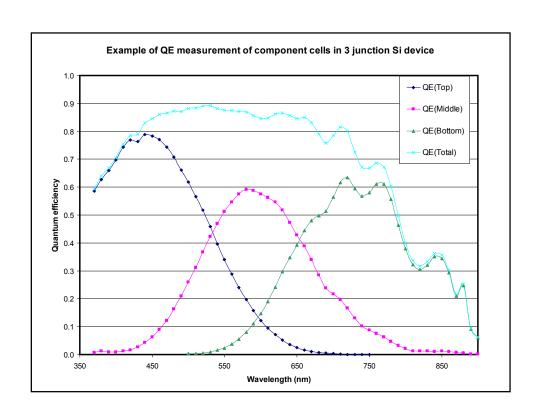
Atomic Force Microscopy of ZnTe Films



AFM images of ZnTe (left) and ZnTe:N (right) films sputtered with $3\% N_2/(Ar+N_2)$ on microscope slides.











Plans for Global Solar Energy's High-Performance Program

Prime Contractor: Global Solar Energy

Lower-Tier Subcontractor: ITN Energy Systems

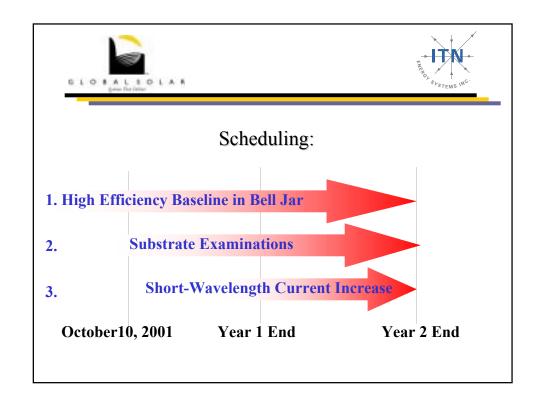
Track: Thin-Film Concentrator





Phase I: How do we move thin film CIGS devices on metal foil toward 20% efficiency?

- 1. Eliminate efficiency differences between devices on steel and on glass.
- 2. Increase short-wavelength current collection.

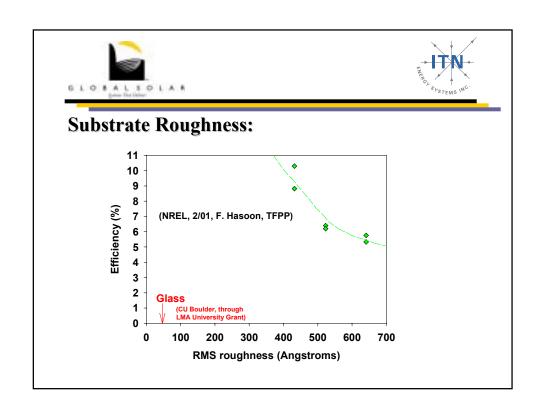


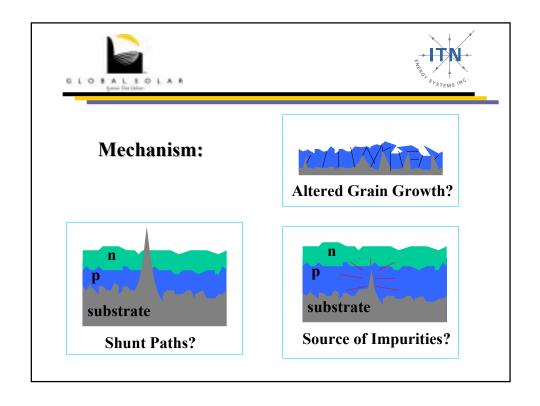




Substrate Examinations: Eliminate efficiency differences between best devices on steel and best devices on glass.

- 1. Substrate roughness
- 2. Harmful impurity diffusion, with and without barriers
- 3. Modified NaF incorporation
- 4. Substrate Temperature









Activities:

Experiments:

AFM

Electropolishing

IV Characterization

Device Fabrication

η vs. roughness. Can we approach smoothness of glass with electropolishing?

Is roughness equally harmful for foils without harmful impurities? (Mo, Ti)

How much does Mo mask the bare substrate roughness?

Does NaF precursor morphology affect growth surface? Is it better deposited after In₂Se₃ growth?





Substrates, Harmful Impurity Diffusion

Activities:

Experiments:

Device Fabrication

Diffusion Barrier Deposition

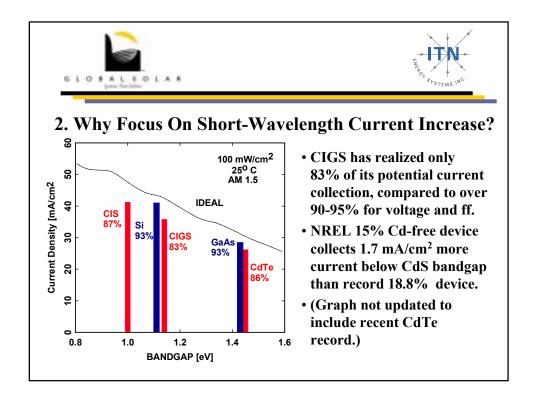
IV Characterization

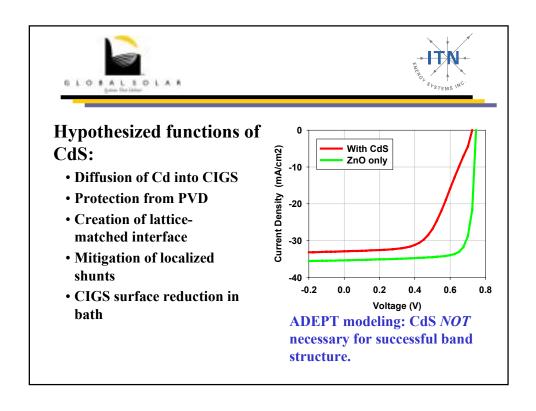
SIMS

Device performance and Fe, Cr, C concentrations vs. Mo thickness

Device performance and Fe, Cr, C concentrations vs. barrier (such as Cr) thickness and properties

Do roughness studies on Mo and Ti foils help quantify effect of impurities?



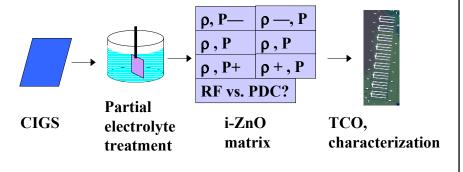


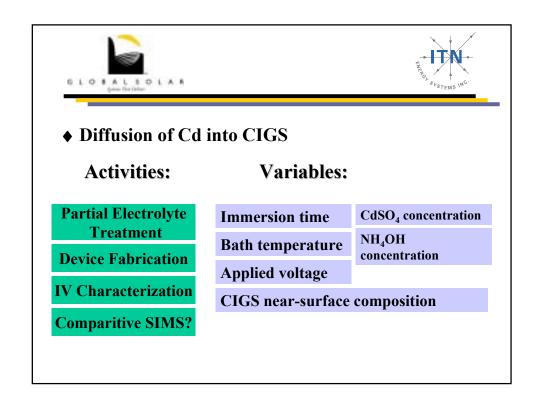




Examine each role, determine importance and how might be replaced with higher-transmission option:

♦ Mitigate localized shunts ♦ Protect from PVD









- ♦ Creation of lattice-matched interface
- ♦ CIGS surface reduction in bath

Deposit very thin CdS layers?

- Slow down bath with lower thiourea concentration or lower temperature.
- Vary thickness by varying sample immersion time.

Valid examination if

- Oxidation creates active defects at CIGS/CdS interface, but not at CdS/ZnO interface.
- Lattice matching-related defects between CdS and ZnO are less active than those between CIGS and ZnO.
- Or, junction is entirely in CIGS.

Otherwise, defects are still very close to junction.

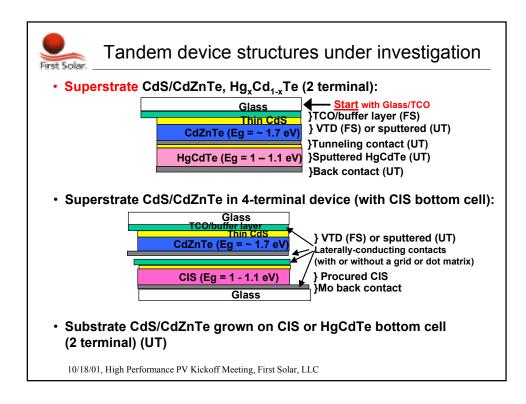


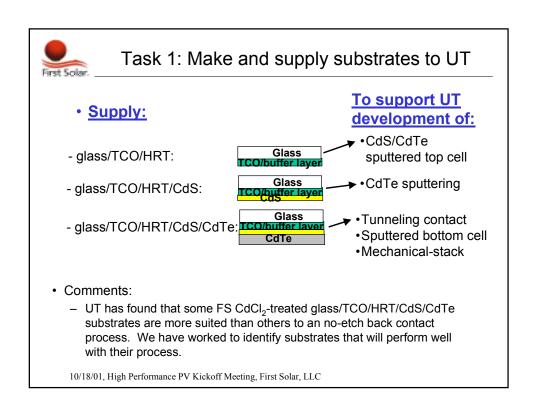


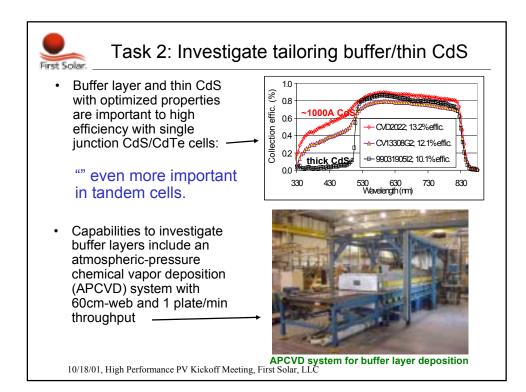
SUMMARY

Program objectives are to:

- 1. Determine the important variables in the efficiency differences between the best devices on glass and the best devices on steel.
- 2. Improve short-wavelength current collection by determining the important functions of the CdS and replacing it.
- 3. Demonstrate at least 16% efficient CIGS devices on metal foil that demonstrate improved blue light collection and insignificant efficiency degradation between glass and steel.









Task 3: Explore feasibility of CdZnTe by VTD

 Task: Explore the feasibility of depositing CdZnTe with controlled levels of Zn with research-sized Vapor Transport Deposition (VTD) system; characterize films by spectrophotometry, EDS and SEM.

Small area VTD system (10 cm x 10 cm samples):



10/18/01, High Performance PV Kickoff Meeting, First Solar, LLC



Other year 1 tasks for First Solar

- Interconnect and laminate top cells with CI(G)S bottom cells for mechanically stacked cells
- Light-soak and characterize single and dualjunction cells developed as part of this effort

10/18/01, High Performance PV Kickoff Meeting, First Solar, LLC



Year 2 tasks for First Solar

- Make and supply samples to UT to support magnetron-sputtered top-cell absorber layer, top-cell-contact, bottom-cell, and mechanical-stacked cell development efforts:
 - glass/TCO/HRT; glass/TCO/HRT/CdS; glass/TCO/HRT/CdS/CdTe
- Optimize HRT/thin-CdS layers for use in top cells with CdZnTe
- Deposit by VTD CdZnTe with controlled levels of Zn for use with two-terminal and four-terminal tandem cells; characterize films by spectrophotometry, EDS and SEM.
- Interconnect and laminate top cells with CI(G)S bottom cells for mechanically stacked cells
- Light-soak and characterize single and dual-junction cells developed as part of this effort

10/18/01, High Performance PV Kickoff Meeting, First Solar, LLC



Some other relevant facilities

· Processing at Technology Center:

- Module scale back-contact deposition and processing equipment
- Sputter-deposition and e-beam evaporation systems
- Ventilation hoods and wet-chemistry for pre- or post-contact treatments
- 2 ovens for heat treatments
- Small-area (100 cm²) APCVD system for specialty buffer layers
- Small-sample encapsulation
- Finishing line at factory for 60 cm x 120 cm modules (1 plate/min designed capacity)

· Accelerated-life testing (ALT) systems:

- Light-soak stations for 400 10cm x 10cm samples and 26 modules
- 3 ovens for dark stress ALT (with N₂ purge and external bias stress options)

· Characterization systems:

- Xenon-lamp solar simulator (measures up to 18 cells at once; tracked in database)
- CV/CF system for carrier-concentration and trap characterization
- Varian Cary 500 spectrophotometer with integrating sphere
- SEM with EDS capability
- Solar cell spectral response measurement system (monochrometer-based with Xenon lamp source)
- X-Y mapping systems with 4-pt resistance, CdS and CdTe thickness by transmission, SPV, and PL of CdTe.

10/18/01, High Performance PV Kickoff Meeting, First Solar, LLC

<u>Development of a II-VI-Based High Performance, High</u> <u>Band Gap Device for Thin-Film Tandem Solar Cells</u>

C. S. Ferekides and D. L. Morel
ELECTRICAL ENGINEERING DEPARTMENT
CENTER FOR CLEAN ENERGY AND VEHICLES
UNIVERSITY OF SOUTH FLORIDA
TAMPA, FL

SUPPORTED BY THE NATIONAL RENEWABLE ENERGY LABORATORY

Department of Electrical Engineering, Center for Clean Energy and Vehicles, Presented at the 1st High Performance PV Meeting, NREL, October 18, 2001.



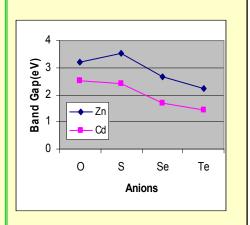
OUTLINE

- > Tandem solar cells
 - → Why? to reach 25% efficiencies
 - → How?
 - Using a 16-18% high band gap top cells
 - Top Eg=1.7 eV, bottom Eg=1.0 eV (CIGS)
 - → II-VI compounds can be used for top cell
- Device structure
- > AMPS Modeling
 - → Bottom cell vs. thickness of top absorber
 - Light J-V
 - SR
 - Jsc and η
 - → Top Cell performance vs. thickness
 - → Tandem Cells
- > Approach



Background

- ➤ The band gap combination 1.7/1.0 eV is near-ideal for a 2-terminal tandem
- CIGS with a band gap of 1.0 eV and an efficiency of 15% is the clear choice for the bottom cell
- To reach 25% the top cell will have to be in the 16-18% range since it will contribute about 2/3rds of the output
- As seen in figure 1, II-VI compounds have the requisite band gaps
- Cd_xZn_{1-x}Te(CZT) covers the range 1.45-2.2 Ev and can capitalize on successes with CdTe technology
- CdSe has a band gap near the ideal of 1.7 eV and is a binary

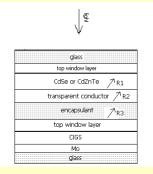


Department of Electrical Engineering, Center for Clean Energy and Vehicles, Presented at the 1st High Performance PV Meeting, NREL, October 18, 2001.



Device Structure

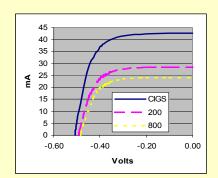
- ➤ A 4-terminal structure is proposed as shown in figure 2.
- > Top Cell: II-VI; Bottom Cell: CIGS
- Preliminary calculations indicate that reflection losses to the bottom cell can be kept to 10%
- Key issue: Transparent Contact for top cell.
- ➤ Window Layers several options
- > Substrates: Glass!





AMPS Simulation - Bottom Cell vs. Thickness of Top Absorber

- Achieved parameters for a 14.7% 1.0 eV CIGS cell are used for the bottom cell (J_{sc} = 42.6 mA/cm², V_{oc} = 508 mV, FF = 0.68,)
- The thickness of the top 1.7 eV cell is varied from 200 to 800 nm.

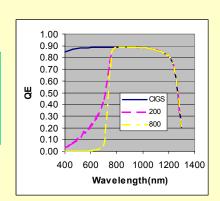


Department of Electrical Engineering, Center for Clean Energy and Vehicles, Presented at the 1st High Performance PV Meeting, NREL, October 18, 2001.



<u>AMPS Simulation – Bottom Cell QE vs. Thickness of Top</u> <u>Absorber</u>

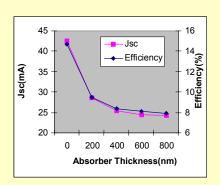
There is a large drop in Jsc for 200 nm, but the drop slows significantly as the top cell thickness is increased to 800 nm





<u>AMPS Simulation – Bottom Cell Performance</u>

- J_{sc} asymptotes toward 25 mA/cm²
- ➤ Efficiency of the bottom cell is in the range of 8-9% for the 400-800 nm thickness range

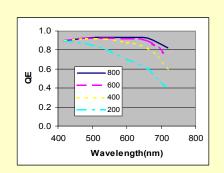


Department of Electrical Engineering, Center for Clean Energy and Vehicles, Presented at the 1st High Performance PV Meeting, NREL, October 18, 2001.

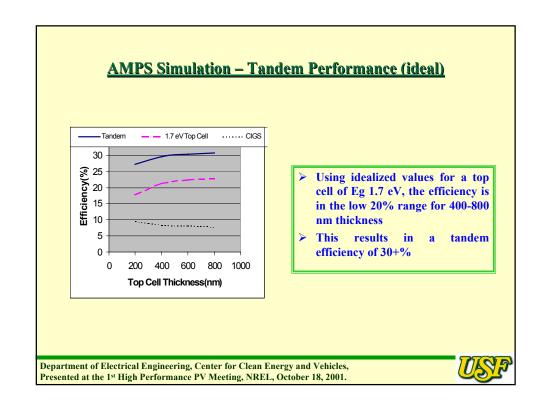


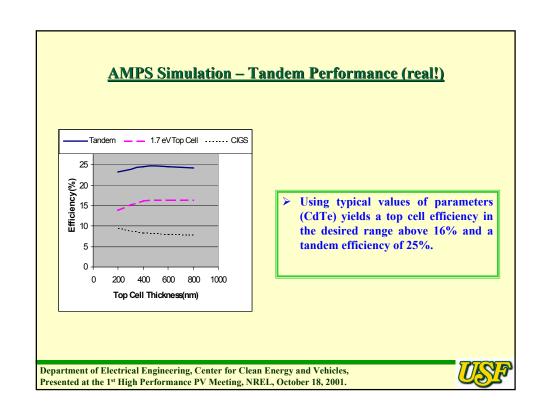
AMPS Simulation - Top Cell QE

Not much loss in the top cell occurs when its thickness is reduced to the 400-600 nm range









Deposition Technology - Current Status

Deposition Technology:

- → New deposition system with co-evaporation capabilities will be constructed for this project
- → Close spaced sublimation (available)
- → RF-sputtering
- ➤ Challenges: develop ABSORBER and its TRANSPARENT CONTACT
- > Project started on 09/01:
 - → Begun deposition of II-VI films using existing deposition capabilities.





NREL High Performance PV Program



Identification of Critical Paths in the Manufacture of Low-Cost High-Efficiency CGS/CIS Two-Junction Tandem Cells

by Oscar D. Crisalle University of Florida Gainesville, FL

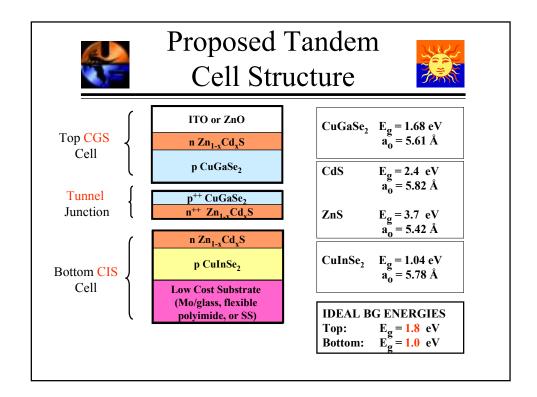
Program Kick-Off Meeting, Golden, CO, October 18, 2001



Principal Investigators



- Prof. Oscar D. Crisalle
 - Chemical Engineering Department
 - Instrumentation, process modeling and control
- Prof. Sheng S. Li
 - Electrical & Computer Engineering Department
 - Device design, characterization, & optimization
- Prof. Tim J. Anderson
 - Chemical Engineering Department
 - Materials growth, characterization, & optimization





Critical Issues



- Tunnel junction:
 - Fabrication of a transparent, low resistance, lattice-matched, and heavily-doped junction.
 - Low temperature deposition process
- Top cell (CGS)
 - Improved conversion efficiency
 - Low temperature deposition process



Approach



- Study each component separately during Phase I
- Reduce complexity by growing single-crystal films on GaAs (use low-cost substrates in Phase II)

ZnO : Al
n Zn _{1-X} Cd _X S
p CuGaSe ₂ ($a_0 = 5.61 \text{ Å}$)
p^+ GaAs ($a_0 = 5.653 \text{ Å}$)

Single-Crystal Substrate

(a) Top-Cell Structure (CGS)

ZnO : Al
$n\ Zn_{1X}Cd_XS$
p CuInSe ₂ ($a_0 = 5.78 \text{ Å}$)
Graded Layer: p Cu(In,Ga)Se ₂
p ⁺ GaAs (a _O = 5.653 Å) Single-Crystal Substrate

Metal contact p⁺⁺ CuGaSe₂ $n^{++}\,Zn_{1\text{-}X}Cd_X^{}S$ $n^{+}\,GaAs$ Semi-Insulating GaAs Substrate

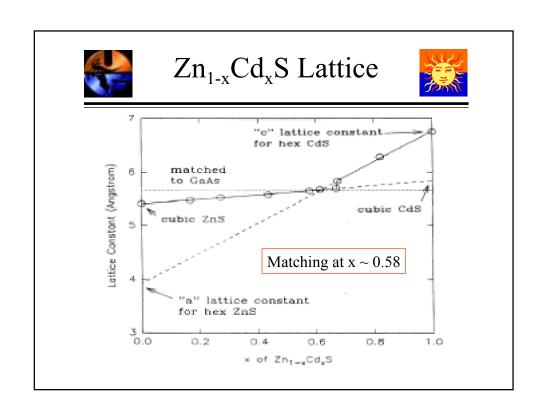
(b) Bottom- Cell Structure (CIS) (c) Tunnel-Junction Structure

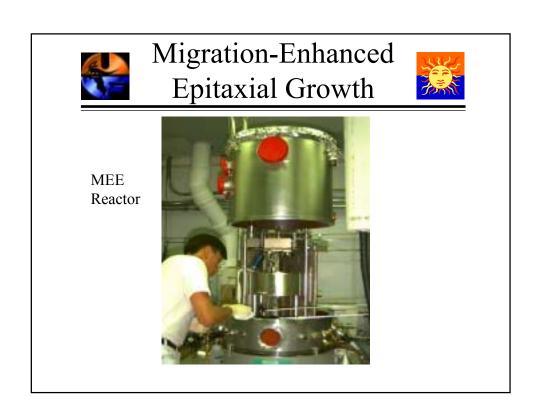


Project Goals



- Identify the critical manufacturing and materials issues for achieving a 25% conversion efficiency CGS/CIS two-junction tandem solar cell.
- Fabricate a bottom CIS cell with a conversion efficiency in the 13% - 15 % range.
- Fabricate a top CGS cell with a conversion efficiency in the 10% - 13 % range.
- Fabricate a low resistance and high quality tunnel junction interconnecting the top and bottom cells.







Bottom CIS Cell - Activities Proposed



ZnO:Al

n Zn_{1-x}Cd_xS

p CuInSe₂

Graded p Cu(In,Ga)Se₂

p⁺ GaAs Single Crystal Substrate

- Take advantage of standard processing technology
- Grow and characterize single-crystal epitaxial CIS by Migration Enhanced Epitaxy (MEE)
- Single-crystal n⁺ GaAs substrates plus graded p Cu(In,Ga)Se₂ (or p In_xGa_{1-x}As) to reduce the dislocation density and grade the band gap energy
- Grow and characterize Zn_{1-x}Cd_xS) layer
- Deposit ZnO:Al layer via sputtering.



... Bottom CIS Cell Activities Proposed



ZnO:Al

n Zn_{1-x}Cd_xS

p CuInSe₂

Graded p Cu(In,Ga)Se₂

p⁺ GaAs Single Crystal Substrate

- Dark- and photo- current-voltage (I-V) and capacitance-voltage (C-V) characteristics and spectral response measurements to determine the quantum efficiency.
- Develop an optimal device model for the bottom CIS cell and identify the physical parameters and optimal thickness of each layer using AMPS-ID device simulation program.



Top CGS Cell - Activities Proposed



ZnO:Al

 $n\ Zn_{1\text{-}x}Cd_xS$

p CuGaSe₂

p⁺ GaAs Single Crystal Substrate Grow single-crystal and epitaxial CGS on single crystal substrates by MEE and characterize the CGS absorber layers.

- Investigate an electrodeposition process for CGS as (low temperature process and low cost alternative)
- Optimize the electronic properties of the CGS absorber layer (net hole concentration ~ 10¹⁶ cm⁻³, mobility, and lifetime).
- Seek to increase the conversion efficiency (consider the addition of S).



... Top CGS Cell -Activities Proposed



ZnO:Al

n Zn_{1-x}Cd_xS

p CuGaSe₂

p⁺ GaAs Single Crystal Substrate

- Investigate temperature, composition and other factors affecting the deposition of the Zn_{1-x}Cd_xS buffer layer
 - Consider CBD and MOCVD processes
- Perform the dark- and photo- I-V characteristics and spectral response measurement of the top CGS cell.
- Develop optimal device model for the top CGS cell and identify the physical parameters & optimal thickness of each layer using AMPS-ID



Tunnel Junction - Activities Proposed



■ Construct a prototype tunnel junction diode

• Determine if CGS can be doped p-type in the 10¹⁸ cm⁻³ range

- Investigate venues for n⁺⁺ Zn_{1-x}Cd_xS layer deposition
- Explore alternatives for n⁺⁺ doping of the Zn_{1-x}Cd_xS layer
 - **▼** Utilize intrinsic defects in CBD processing
 - ▼ Utilize extrinsic dopants (group III (Ga or In) or halide atoms (Cl, Br, or I)).



p⁺⁺ CuGaSe₂ n⁺⁺ Zn_{1-x}Cd_xS

n+ GaAs

Semi-Insulating GaAs Substrate



... Tunnel Junction - Activities Proposed



Metal contact

p⁺⁺ CuGaSe₂ n⁺⁺ Zn_{1-x}Cd_xS

n+ GaAs

Semi-Insulating GaAs Substrate

- Characterize the effect of processing via I-V characteristics and the peak current density under different annealing temperatures and duration
- Characterize the n⁺⁺ Zn_{1-x}Cd_xS layer by XRD, XPS, SEM, and Hall and transmission measurements
- Develop a tunnel junction diode model and identify the physical parameters and optimal thickness of the tunnel junction layer using AMPS-ID.



Milestones-Phase I Y1



- Produce epitaxial CIS and CGS on lattice-matched substrates.
- Characterize electrical and optical properties of the tunnel junction; determine the optimal thickness/doping level for low optical & electrical losses.
- Develop device model for each component of tandem cell using AMPS-1D device simulation program and predict single-junction cell performance.
- Develop and evaluate a one-step electrodeposition method for growing CGS films.



Milestones-Phase I Y2



- Identify growth parameters for a 10% 13% efficiency CGS cell
- Identify growth parameters for a 13% 15% efficiency CIS cell
- Identify growth parameters for a low resistance tunnel junction.
- Develop a CGS/CIS two-junction tandem solar cell model by AMPS-1D and identify an optimized tandem cell structure with a 25 % efficiency.
- Develop and evaluate a two-step electrodeposition method for growing CGS films.

CulnSe₂ as a Component in High Efficiency Concentrator Photovoltaics

A. Rockett, D.X. Liao, and C.M. Mueller

University of Illinois, Department of Materials Science and Engineering,

1-107 Engineering Sciences Building, MC-233, 1101 W. Springfield Ave., Urbana, IL 61801 arockett@uiuc.edu 217-333-0417



University of Illinois at Urbana Champaign College of Engineering Department of Materials Science and Engineering



Epitaxial CulnSe₂ for Ultrahigh Performance Solar Cells

Multijunction solar cells have achieved very high performances with junctions in excess of 1 eV.

Ultimate performances can only be achieved with a 1.0 eV cell.

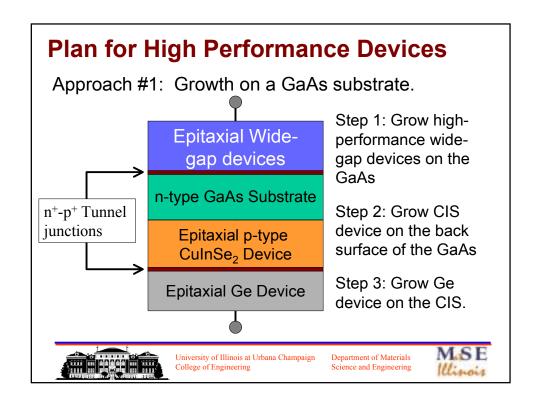
CulnSe₂ is an excellent candidate for this application:

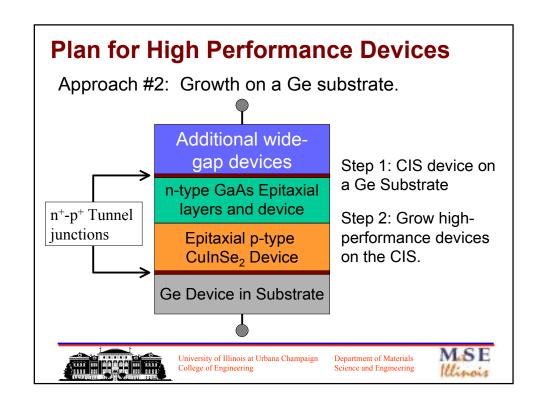
- · 1.0 eV energy gap
- · Has acheived very high-performance single junctions
- Epitaxial growth has been demonstrated on various surfaces.



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Plan for High Performance Devices

Approach #1: Growth on a GaAs substrate:

Advantages:

- Can begin with a known high performance device
- CIS can be grown on relatively lower quality back surface of the wafer.
- Segregation of impurities from the CuInSe₂ into the Ge can probably be stopped with barriers.
- Ge growth requires a relatively low temperature.

Disadvantages:

- Requires a process that the high-performance devices can withstand.
- Defects are likely in the Ge device.



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Plan for High Performance Devices

Approach #2: Growth on a Ge substrate:

Advantages:

- Indirect Ge requires a thick layer. Substrate is ideal for this
- All growth on the same side of the substrate.

Disadvantages:

- Potential for detrimental segregation of material from CulnSe₂ into the GaAs can be disastrous.
- Difficult to grow extremely high-performance GaAs on CulnSe₂. May lead to poor high gap device junctions.



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Experimental Plan

Determine the performance that can be achieved in CIS junctions

on n+ GaAs:

 Establish the quality of solar cells that can be produced from epitaxial CIS layers using a p-CIS n-GaAs heterojunction for current collection.

on p⁺ and n⁺-Ge:

- Establish conditions for epitaxy on Ge.
- Produce solar cells in collaboration with NREL and IEC on these layers.
- Send completed layers to NREL as substrates for highperformance device growth.



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Progress to date:

n+ GaAs Substrates

- Substrates obtained with various orientations
- Epitaxial layers grown
- Preliminary results show diode-like behavior.

p⁺ and n⁺-Ge Substrates:

Substrates on order.

Additional ultrahigh vacuum deposition system is being put into order for CIS growth to provide cleaner films and larger substrates (for compatibility with NREL growth systems).



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Issues:

n+ GaAs Substrates

- Growth of Ga-free CIS for 1 eV gap. Ga diffusion from the GaAs substrate could be a problem.
- Deposition conditions exist to minimize diffusion.
- Diffusion within the depletion width of the junction could be a positive effect
- Need to avoid Cu diffusion into the GaAs.
- Choice of substrate orientation (for junction properties and epitaxial temperature).
- · CIS-GaAs heterojunction performance as a solar cell.

p+ and n+-Ge Substrates:

- Past efforts have shown problems with epitaxy on Ge. Extensive work was never performed to determine opportunities.
- · And similar issues as above.



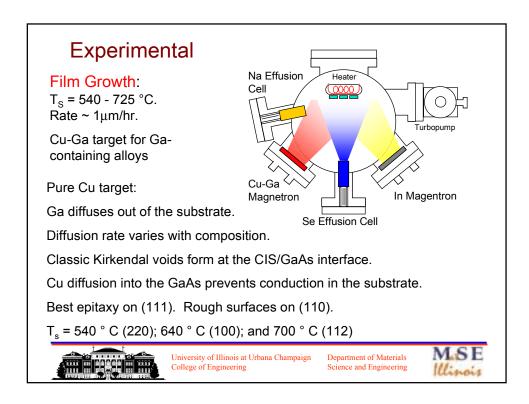
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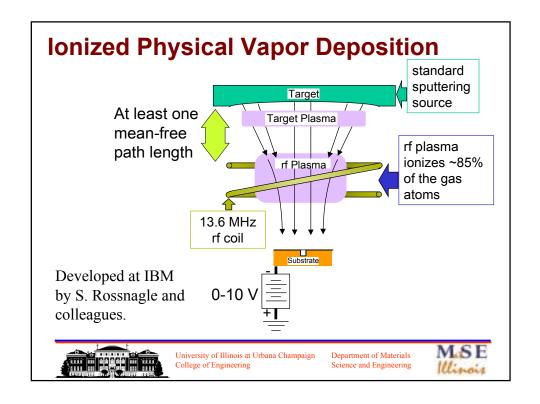
Our "Beyond the Horizons" program should also contribute to the Thin Film HiPer program...

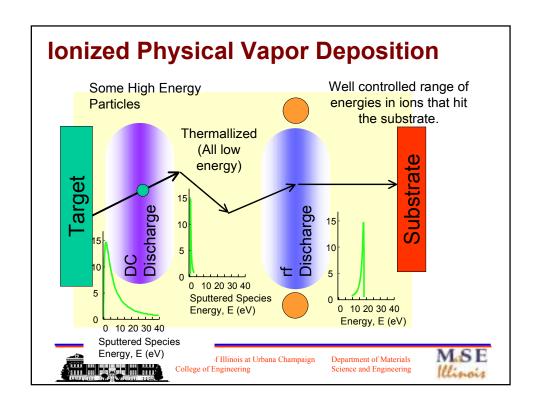


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Ionized Physical Vapor Deposition

Advantages:

- · Simple add-on to existing deposition environments
- · Most atoms hit the surface with moderate kinetic energy
- Energy can be controlled to be below the damage threshold but well above the energy needed for surface diffusion and cluster disruption.
- By control of the atom impact energy we can make atoms go where they normally would rather not go without forcing them into defect sites.
- Supply of energy to the growth surface can replace the energy normally supplied by heating.



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Experimental Plan

Modify existing deposition systems for ionized physical vapor deposition:

- Existing growth conditions can be used and normal growth continued to show deposition condition stability.
- Add rf coil in front of the substrate in both the existing and the new ultrahigh vacuum deposition systems.
- After establishment of growth conditions, turn on the rf power and show the changes which can be achieved in deposition conditions.
- Produce solar cells from resulting materials for comparison with existing results.



University of Illinois at Urbana Champaign College of Engineering Department of Materials Science and Engineering



Current Status

- · Contract in place.
- New graduate student hired
- Training in progress
- Parts being assembled for new ultrahigh vacuum deposition system to increase materials production capability



University of Illinois at Urbana Champaign College of Engineering Department of Materials Science and Engineering



<u>High-Performance PV Kickoff Meeting (NREL, Denver, 18 Oct 2001)</u>

InGaP/GaAs-on-Ceramic Thin-Film Monolithically Interconnected, Large-Area, Tandem Solar Cell Array

Phase I:

"Development of Low-Cost Substrates and Deposition Processes For High-Performance GaAs-Based Thin-Film Solar Cells"

MICHAEL G. MAUK, BRYAN W. FEYOCK, and JEREMY BALLIET AstroPower, Inc.
Solar Park, Newark, Delaware USA 19716-2000

Tel: 302-366-0400 ext 133

Fax: 302-368-6474

e-mail: <u>mauk@astropower.com</u> website: www.astropower.com

Broad Objective

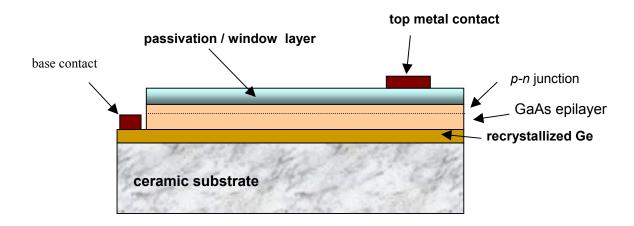
To transfer space solar cell technology that currently yields record-efficiency solar cell performance to a low-cost structure for terrestrial applications

Basic Approach

- replace the expensive single-crystal Ge (or GaAs) substrate wafer with a cheaper alternative
- develop low-cost, high-throughput expitaxy / deposition processes in place of MOCVD
- achieve high efficiencies in GaAs/InGaP solar cells based on thin-films of large-grain (> 1mm) polycrystalline GaAs (and Ge)

Short-Term Goals

- recrystallize Ge films on alumina ceramic substrates to yield highly-oriented, smooth, low-defect layers with grain sizes >> 1 mm
- characterize Ge films electrically and structurally
- use Ge-on-ceramic structures as substrates for GaAs epitaxy. Ge functions as a "seeding layer" for epitaxial growth of GaAs



Criteria for High-Efficiency GaAs-Based Solar Cells

- Close thermal expansion match between substrate / GaAs
- Grain Size > 1 mm
- Close (< 1%) Lattice Matching of Epilayers
- Suppress Silicon and Germanium Autodoping (from Substrate)
- Defect Densities (< 10⁵ cm⁻²)
- Suppress antiphase domains (GaAs/Si or GaAs/Ge)
- Front surface passivation or wide-bandgap window layer
- Back surface field or wide-bandgap cladding layers
- Possibly include light trapping (back reflector or front texturing)

New Features

- 1-micron thick undoped "space-charge" layer to reduce tunneling currents [Research Triangle Inst.]
- post-growth grain boundary passivation (plasma H⁺ treatments, sulfur or selenium treatments [Milnes et al.], Zn diffusion in grain boundaries)
- all-top contact design for monolithic interconnection (exploiting electrically insulating ceramic substrate)
- buried conducting layer (heavily-doped Ge)

Emitters/Front Surface Passivation

- AlGaAs wide-bandgap "window" layer (LPE or MOCVD) [Hovel]
- InGaP emitter or window (LPE, MOCVD, CVD, CSVT)
- Thin (<100 Å) homojunction [FAN]
- ZnSe emitter or window [BLISKE]
- GaAsP emitter or window [BARNETT AND PAREKH]
- Phosphorus "exchange" reaction $P + GaAs \rightarrow GaAsP$
- Aluminum "exchange" reaction Al + GaAs → AlGaAs
- Anodic oxidation, SiNx CVD
- Chemical surface treatments (Se, S, etc..)
- Zn-diffused, anodic oxide emitters [SULIMA]
- Transparent Conducting oxides for current spreadings and ARCs [COUTTS]
- Plasma-enhanced CVD of SiC [IEC]

Ceramic Substrates

- Mixtures of Al₂O₃/SiO₂ for TEC match to GaAs/Ge
- Impurity effects
- Smoothness, wetting properties of molten Ge

Epitaxy Options

GaAs: CSVT or CVD with H_2O , I_2 or HCl as transport agent Ge: CSVT or CVD with H_2O , I_2 or HCl as transport agent ZnSe (as a passivating layer): CVT with H_2 GaAs, AlGaAs, InGaP, GaAsP

- LPE
- Hydride or Halide CVD
- MOCVD by outside group

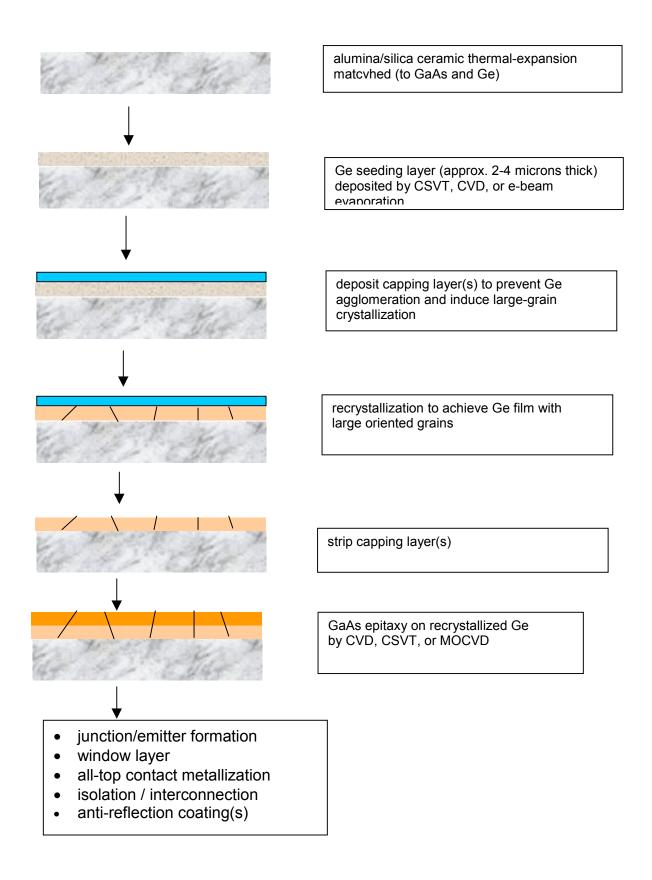
Some Criteria for Epitaxy

- Large scale: substrate size > 10-cm x 10-cm
- Atmospheric pressure operation
- No toxic precursors or by-products
- Inexpensive precursors
- Potential for continuous-mode operation
- Fast deposition rates (> 1 micron/min)
- Low capital equipment cost
- Reasonable control and instrumentation
- In-situ diagnostics
- Avoid halogens (corrosion problems, unwanted co-transport)

Some Related Applications

- InGaP / Si cells for photoelectrochemical hydrogen generation
- Large-area LEDs and detector arrays
- Space solar cell arrays

Thin-Film Poly GaAs-on-Ceramic Solar Cell Process



Reversible-Transport Reactions for Deposition of Silicon, Germanium, and GaAs and Their Applications to Solar Cells

Silicon: Si + $I_2 \leftrightarrow Sil_2$

Germanium: Ge $+ I_2 \leftrightarrow GeI_2$

 $Ge + H_2O \leftrightarrow GeO + H_2$

GaAs: GaAs + $H_2O \leftrightarrow GaO + \frac{1}{2}As_2 + H_2$

 $GaAs + \frac{3}{2}I_2 \leftrightarrow GaI_3 + \frac{1}{2}As_2 + H_2$

<u>Advantages of Reversible Transport Reactions for Si, Ge and GaAs CVD</u>

- Fast Deposition Rates (5 to 10 microns/min)
- Transport agent stored as a solid or liquid
- Recycled Transport Agent
- Atmospheric-Pressure Operation
- No Highly Toxic Precursors or Byproducts
- High-Deposition Efficiency (esp. Close-Spaced Configuration)
- Simple Control (source temp, substrate temp, transport agent conc.)
- Doping from Source

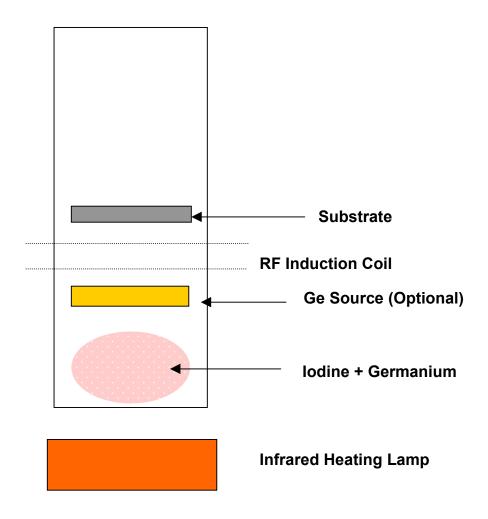
Silicon and Germanium

- Annealed / Recrystallized
- Post-Growth Gettering
- Less-Pure Solid Sources

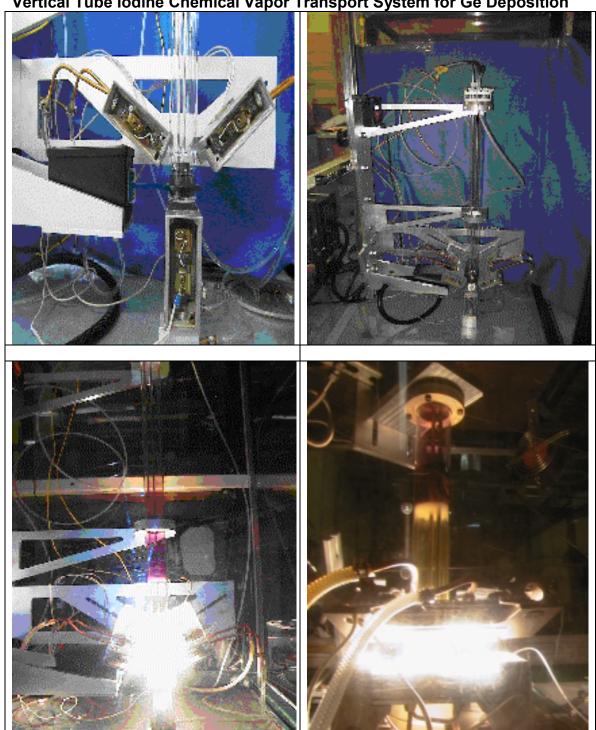
Issues

- Semiconductor Purity
- Co-Doping (from substrate)
- Grain-Size and Texture, Electrical Activity of Grain Boundaries

Vertical Tube Iodine Chemical Vapor Transport System for Depositing Ge on Ceramic Substrates

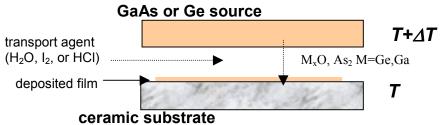


Vertical Tube Iodine Chemical Vapor Transport System for Ge Deposition

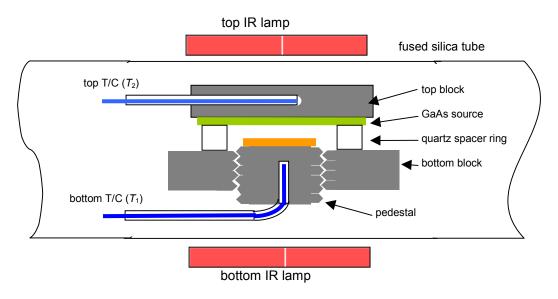


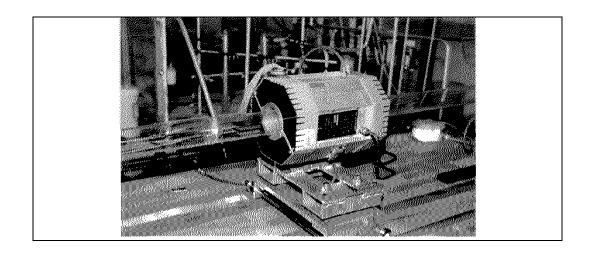
CSVT (Close-Spaced Vapor Transport)

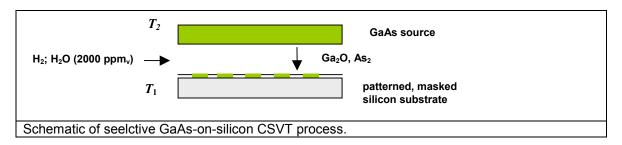
Schematic

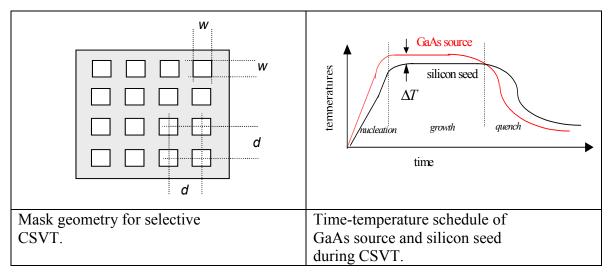


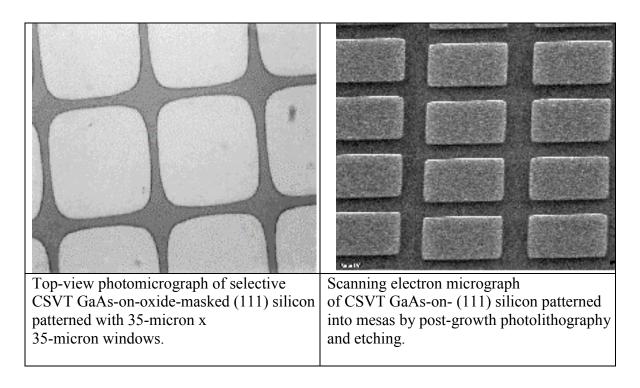
Detail

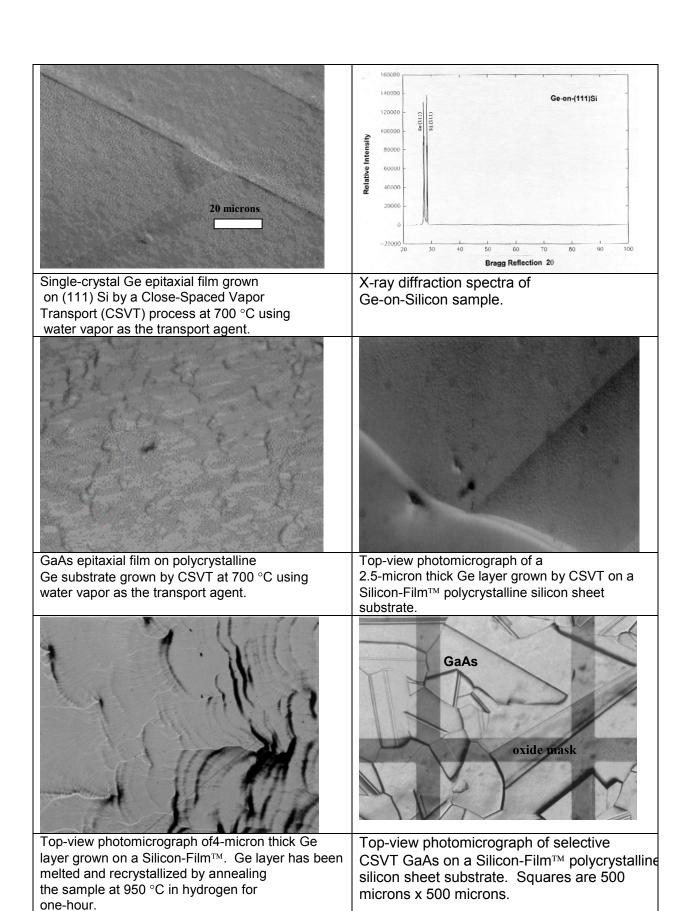


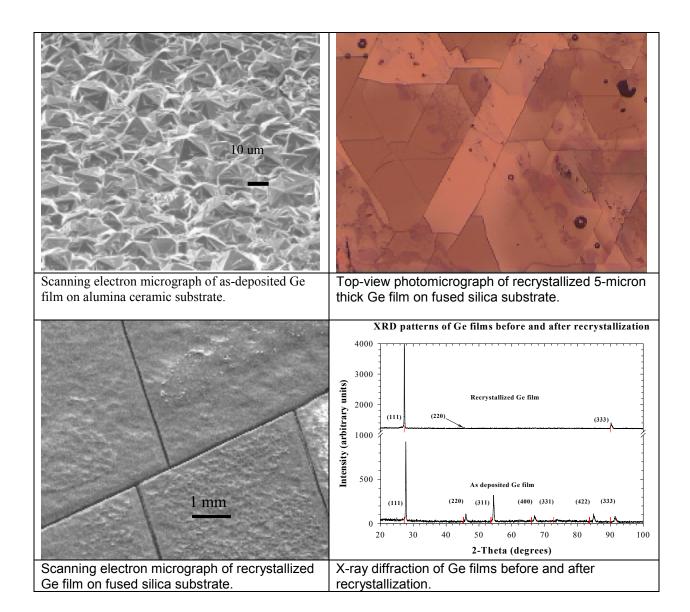


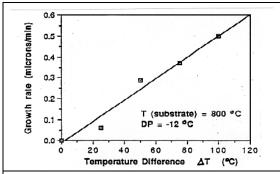




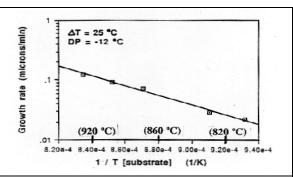




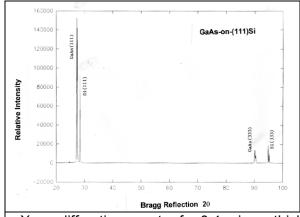


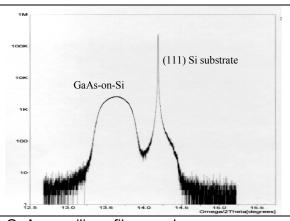


GaAs-on-Si CSVT kinestics as a function of temperature difference ΔT between source and seed.

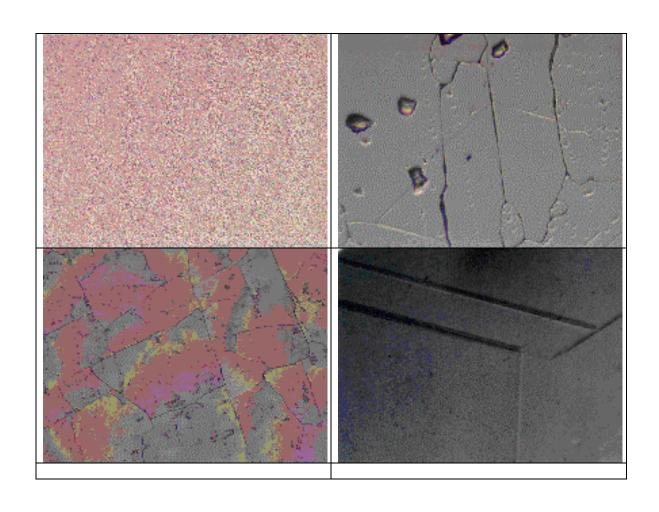


Log of growth rate vs. reciprocal of substrate temperature for GAAs-ON-SI CSVT.





X-ray diffraction spectra for 0.4-micron thick GaAs-on-silicon films made by CSVT. **a.** wide scan, low resoluton, **b.** double-crystal rocking curve.







NREL Basic Research Toward a 40% Efficiency

Sarah Kurtz

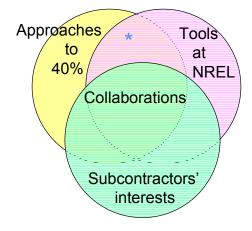
High-Performance PV Kickoff Meeting

October 18, 2001

wated for the U.S. Department of Energy by Midwest Research Institute - Rettelle - Report



Where we fit in the big picture





Opportunities for collaborations between NREL and subcontractors

- TEM characterization of mismatched layers
 - Mowafak_AlJassim@nrel.gov (303)384-6602
- Cell and module measurements
 - Keith_Emery@nrel.gov (303)384-6632
- Characterization (TRPL, DLTS, FTIR, SIMS, etc.)
 - www.nrel.gov/measurements
 - Pete_Sheldon@nrel.gov (303)384-6533
- Resource assessment (spectrum of direct beam)
 and how it affects cell design (AM1.5Direct is not
 appropriate for outdoor concentrator measurements)

Where we fit in the big picture Approaches Tools at NREL Collaborations Subcontractors' interests

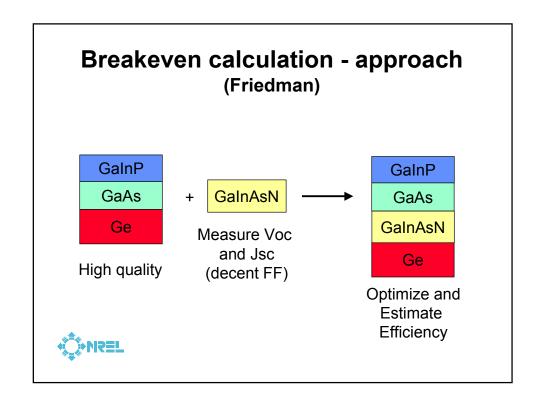
Dilute-nitrogen alloys (GalnAsN, etc.)

(one of many possible approaches)

Problem = Photovoltaic quality is degraded by nitrogen

Reasons for studying this approach:

- GaInP/GaAs/GaInAsN/Ge theoretically > 50%
- GalnP/GaAs/GalnAsN/Ge is similar to GalnP/GaAs/Ge
- "best" GalnAsN cells are close to "break even"
- This basic research problem is well matched to
 NREL's mission and strengths



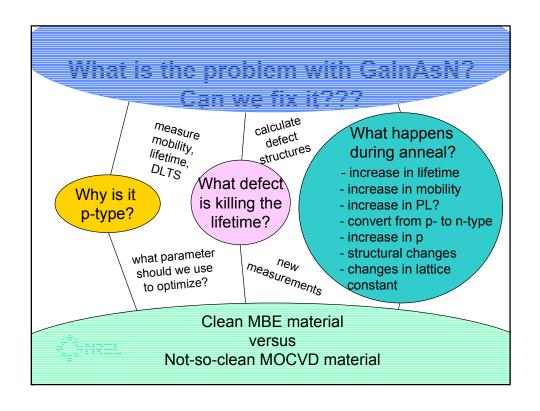
Breakeven calculation - results (Friedman)

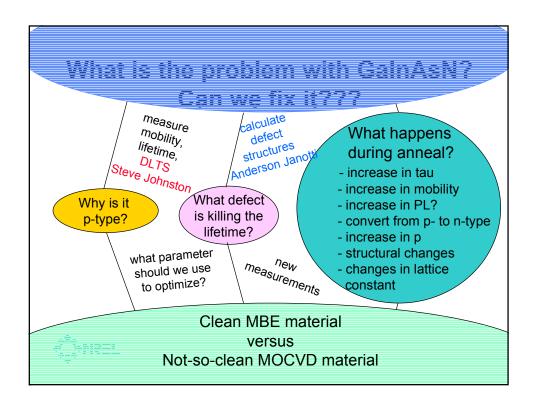
Conditions	Expected 3-junc.	Es fmated 4-junc.
	Effi dency	Efficiency wth
	(no series es.)	be st GalnAsN
AM0 – 1 sun	29.9%	28.2%-29.4%
AM1.5 Gl dbal	40.2%	38.6%-39.7%
(500 suns)		
AM1.5 Direct	38.2%	39.1%-40.5%
(500 suns)		

Conclusion: optimist - we're almost there

pessimist - it's not hopeless







Discussion topics

- Collaboration on materials measurements
- Spectral issues
- Stability/degradation issues (heat, high current, intense light; will it last for 30 years?)







Reference Conditions for PV Concentrators?

Keith Emery National Renewable Energy Laboratory 1617 Cole Blvd., Golden CO 80401 email: keith emery@nrel.gov





Numeral Column Inter Phinocockeae's Concentrator Standards

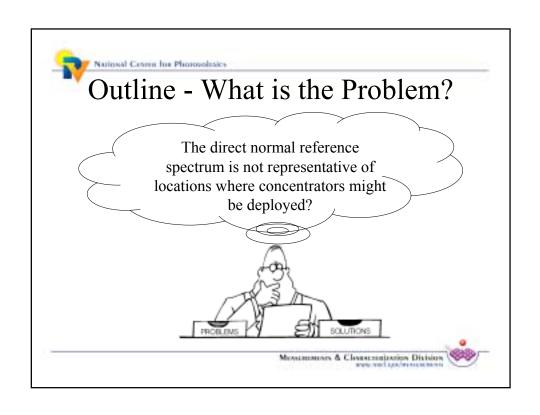
Cells - From ISE, NREL, Sandia, Progress in PV 25 °C, 1-sun = $1000 \text{ W/m}^2 \text{ total irradiance}$ ASTM E891 direct normal spectrum (767 W/m² direct) Area = total area minus peripheral bus bars

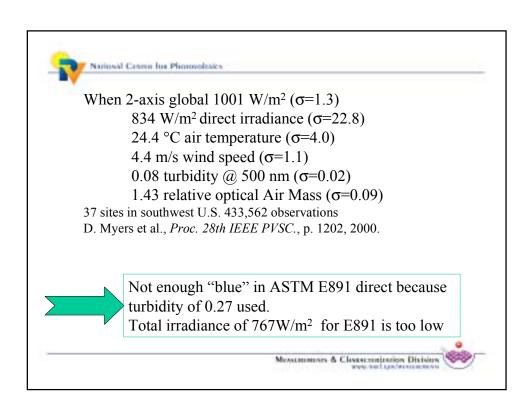
Modules -

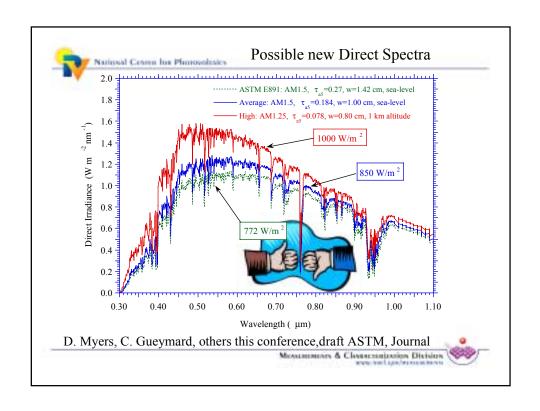
Area = lens/mirror aperture area Prevailing spectral irradiance, 850 W/m² total direct irradiance, 20° air temperature (ASTM draft E130, PVUSA, IEEE 1513) Prevailing spectra corrected to AM1.5, 850 W/m²

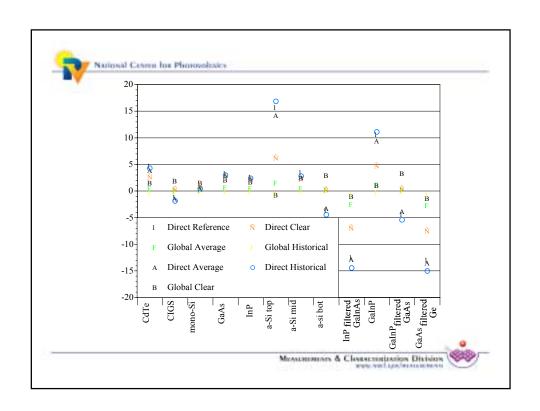
direct irradiance, 150 W/m² diffuse irradiance, 50 °C cell temperature, (IEEE 1513)

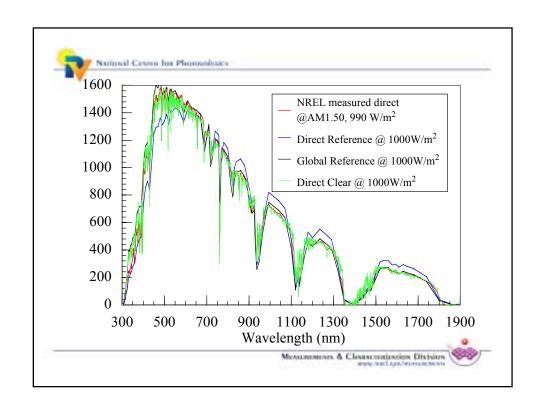
Measurements & Characteristization Division

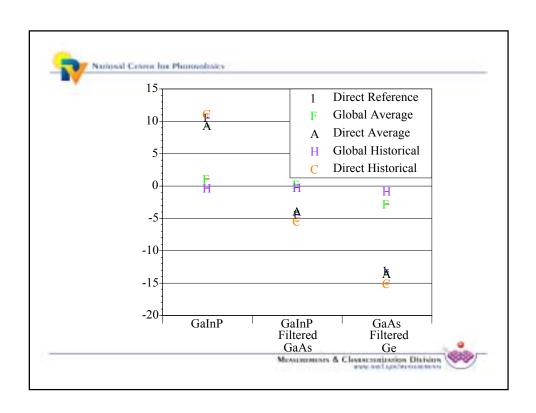


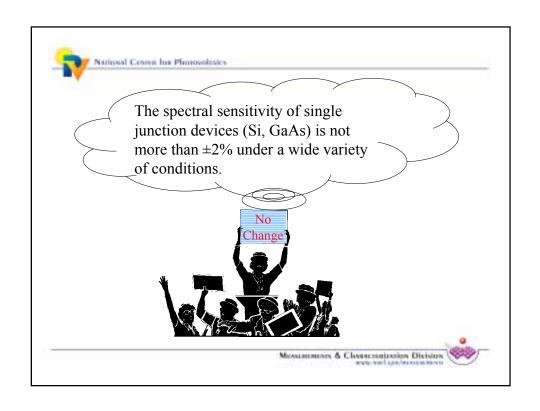


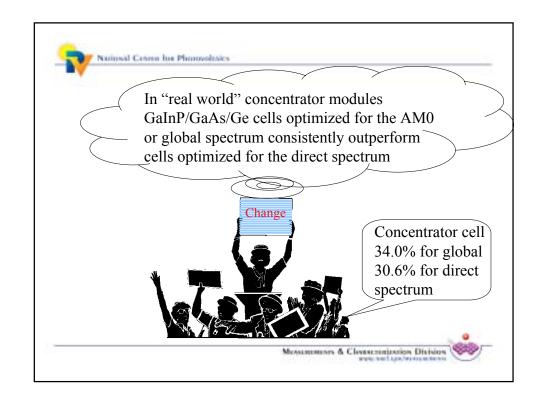














Where do we go from here?

Concentrator modules will be evaluated outdoors under prevailing clear-sky conditions without a correction to the existing direct reference spectrum. At most a correction to AM1.5 (IEEE).

Cells for concentrator modules should be optimized for maximum module efficiency (direct average or global reference).

Report concentrator cell efficiencies under the global and direct reference spectrum.





Where do we go from here?

Get the standards labs together (in person or via email) to make a decision to change the direct reference spectrum to reflect a more realistic turbidity.

Get the U.S. concentrator industry and the national labs (Sandia and NREL) to reach a consensus on what to do.



Measurements & Classicalization Division



High Performance, Low Cost III-V PV Concentrator Module

Raed A. Sherif & Richard R. King
October 18, 2001

Goal & Work Scope

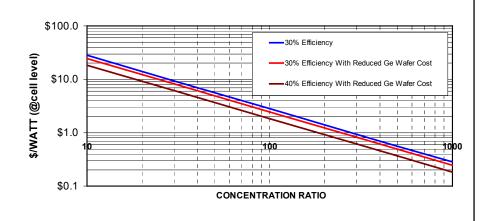


- Identify the critical paths towards achieving a 33% module conversion efficiency at \$1 per Watt.
- A two-year effort investigating:
 - How to increase III-V PV cell conversion efficiency
 - How to make III-V PV cells more robust for high concentration
 - How to reduce the manufacturing costs of III-V PV cells
 - Module designs that are low cost, yet reliable, and have low thermal resistance to the cooling fluid.

Cost Reduction Approaches for III-V PV Cells



- 1. Increase PV cell conversion efficiency & reduce its manufacturing cost
- 2. Use higher concentrations



Specific Objectives in this framework



• PV Cell

- 1. Develop a high efficiency (> 32%) monolithic multijunction PV concentrator cell
- 2. Develop a very high efficiency (> 35%) integrated multijunction PV concentrator cell using two or more different substrates.
- 3. Develop a robust cell structure and metallization scheme that eliminates cell shunting under high concentration.
- 4. Research low-cost substrates for high efficiency multijunction cell growth.
- 5. Added task: deliver 100 concentrator cells (~ 1x4 cm) to operate at ~ 440 suns average with maximum concentration of ~ 1600 suns on 1/4 of the cell.

PV Receiver

- 1. Identify designs that have potential for low cost, high performance and good reliability
- 2. Build prototypes and demonstrate their performance and reliability

High Efficiency Cell Development Work Scope



 Develop a high efficiency concentrator cell (> 32%) by leveraging PV technology used in space applications.

Triple junction cells: GalnP/ GaAs/ Ge
 Metamorphic triple junction cells: GalnP/ GalnAs/ Ge

- Four junction cells: GalnP/ GaAs/ GalnNAs / Ge

• Demonstrate new prototype cell designs using an integrated cell approach (wafer bonding or mechanical stacking) to achieve ultra-high cell efficiency (> 35%)

- Baseline: GalnP/ GaAs/ Si
- Additional examples: GalnP/ GaAs/ Si/ GaSb

GalnP/ GaAs/ GalnPAs/ GalnAs

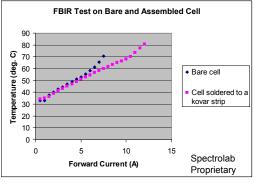
-Two terminal, 3-terminal, and 4-terminal MJ cell designs

Spectrolab Proprietary

Increased Cell RobustnessInteraction with Thermal Management

SPECTROLAB nent AMERICANA

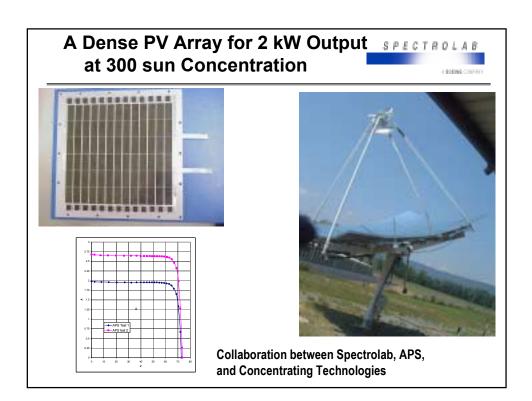
- · Investigate metallization structures that can increase cell robustness to shunting.
- Investigate impact of highly non-uniform flux on a cell level
- Investigate interaction between how a cell is cooled and its current carrying capability.



Module Design



- · Goal is to have a module design that is:
 - Low cost
 - Enables high concentration
 - Enables mechanized assembly, inspection, and test.
- Identify tradeoffs in module cost, thermal management, and thermal expansion mismatches.
- Investigate different types of designs:
 - Single cell vs. densely packed array designs
 - Active vs. passive cooling
 - Which design(s) are more compatible with mechanized assembly, inspection, and test.
- Define an appropriate test vehicle for testing cells under high concentration.



Nov '01 Dec '01 Jan '02	
	Mar '02
	Jul '02
	Jul '02
_	

Tasks & Schedule for Receiver Development (Years I&II)	ECTROLAB
Thermal, thermal stress, electrical and cost modeling for concentrator cells and modules.	Nov' 01
 Preliminary design definitions of prototypes and test vehicle 	Dec' 01
Parts procurement and fabrication	Apr' 02
• Build test vehicle	Jun' 02
• Cell design and fabrication	Oct' 02
• Build prototype	Mar' 03
Demonstrate performance and reliability	Jul' 03



DEVELOPMENT OF TERRESTRIAL CONCENTRATOR MODULES USING HIGH-EFFICIENCY MULTI-JUNCTION SOLAR CELLS

Funded in Part by NREL/ENTECH Subcontract No. ADJ-1-30620-02

Mark O'Neill

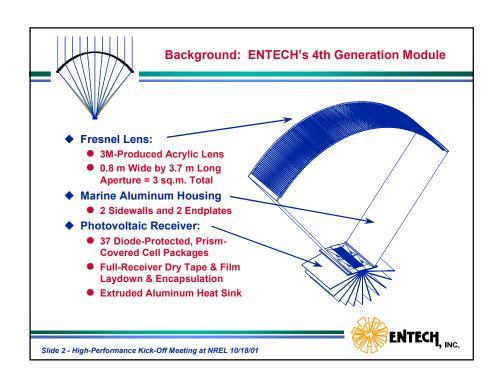
ENTECH, Inc. 1077 Chisolm Trail Keller, TX 76248 USA

Tel: 817-379-0100 Fax: 817-379-0300

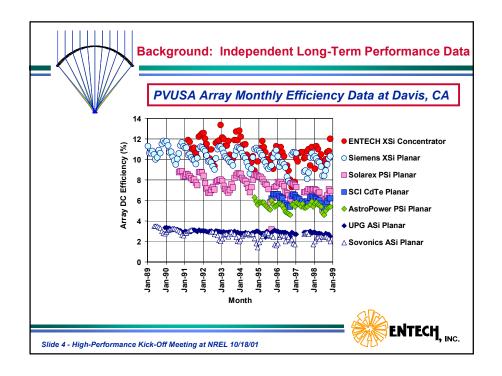
Web: www.entechsolar.com E-Mail: mjoneill@entechsolar.com

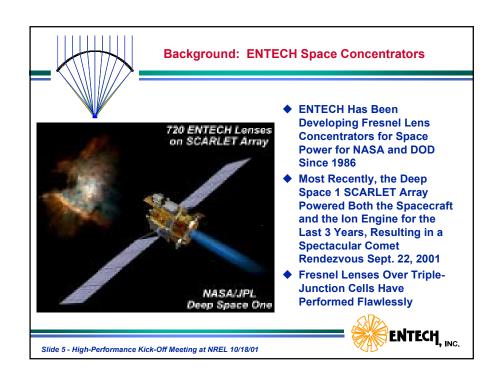


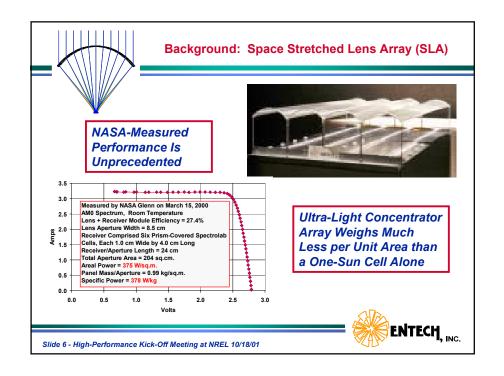
Slide 1 - High-Performance Kick-Off Meeting at NREL 10/18/01













Synergy Between Space and Ground Concentrators

- Space Stretched Lens Modules with Space Multi-Junction Cells Perform Spectacularly Outdoors Under Terrestrial Sunlight
- ◆ All Three Modules Below Exceed 27% Operational Efficiency
- ◆ NREL Confirmed Left Module as World-Record Holder

Spectrolab Cell



EMCORE Cell

TECSTAR/JX Crystals Cells







Slide 7 - High-Performance Kick-Off Meeting at NREL 10/18/01

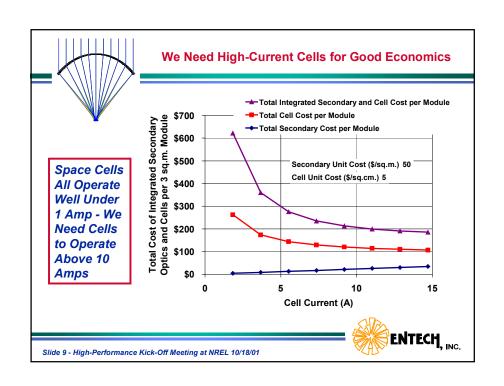


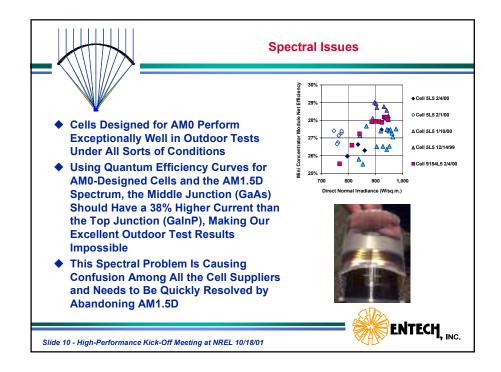
ENTECH's High-Performance Module Development

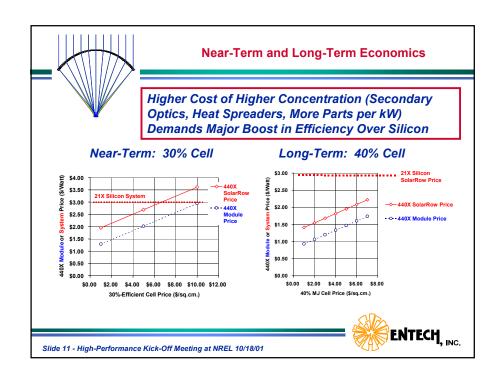
- Since 1999, We Have Been Working on the Development of Terrestrial Concentrator Modules Using Multi-Junction Cells
- ♦ Our Proprietary New 440X MJ Cell-Based Module Will Be a Plugand-Play Replacement for Our Silicon-Based 21X Module, Enabling Use of Proven SunLine and SolarRow Arrays
- ♦ Secondary Optics Will Focus Sunlight in the Longitudinal Direction by Another 21X, Resulting in a 440X Overall Concentration
- ◆ Prototype Testing Indicates that Optical and Thermal Issues Are Technically and Economically Solvable for the New Module
- ♦ However, Cell Issues Remain, But Non-Disclosure Agreements with Cell Suppliers Prevent Open Discussion of Details on These Issues



Slide 8 - High-Performance Kick-Off Meeting at NREL 10/18/01







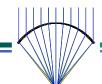


On-Going Work and Key Issues

- ◆ Optimize Operational Current Level for Multi-Junction Cells
 - Is There an Upper Current Limit for Durability?
- ◆ Optimize 440X Module Optics, Including Color-Mixing Lenses
 - Sizing Depends on Current Level Mentioned Above
- **♦ Optimize 440X Module Cells**
 - What Spectrum Is Appropriate for Current-Matching the Junctions?
- ◆ Optimize a Robust 440X Photovoltaic Receiver
 - What Caused Recent Boeing and Loral Array Problems on Orbit?
- ♦ Build and Test Full-Size 440X Concentrator Module & SunLine
 - Schedule Depends on Previous Items
- ► Commercialize 440X SolarRow and SunLine Systems
 - What Price/sq.cm. Can Multi-Junction Cells Reach?

ENTECH, INC.

Slide 12 - High-Performance Kick-Off Meeting at NREL 10/18/01



Recommendations Relating to Multi-Junction Cells

As Concentrator Module Developers and Manufacturers, We Need Help from Cell Suppliers and NREL in Two Key Cell Areas

- Solar Spectrum
 - Near-Term: Cell Suppliers Should Optimize Cells for AM1.5G or AM0, Not AM1.5D, Which Is Not Realistic
 - Mid-Term: NREL Should Consider Deploying Triple-Head Pyrheliometers (GalnP, GalnP-Filtered GaAs, and Black Body Sensors) at Various Sites Around the U.S. and Collect Data
- Cell Durability
 - Near-Term: NREL Should Consider Coordinating Efforts with Spectrolab, EMCORE, TECSTAR, JX, and Other Cell Suppliers to Address Common Issues (Current Limits, Reverse Bias Effects, Transient Effects, Long-Term [30 Years] Durability in Terrestrial Environment, etc.)

Slide 13 - High-Performance Kick-Off Meeting at NREL 10/18/01





Conclusions

- ◆ We Have Already Seen Cell Efficiencies Above 30% and Operational Mini-Module Efficiencies Above 27% in Outdoor Tests of Low-Concentration Space Lenses Over Space Cells
- ◆ These High Efficiencies Could Lead to Outstanding System-Level Economics, If Remaining Issues Are Resolved
- Significant Cell Issues Remain to Be Addressed:
 - Performance Under High, Non-Uniform Concentration
 - Absolute Current Limits Consistent with 30-Year Life
 - Durability Under Continuously Transient Conditions for 30-Year Life
 - Cell Package Cost, Including Interconnects, Heat Spreader, Bypass Diode, Encapsulation, etc., Must Be << \$10/sq.cm. (Current Space)
 - Potential Customers Always Ask for Long-Term Field Data

ENTECH, INC.

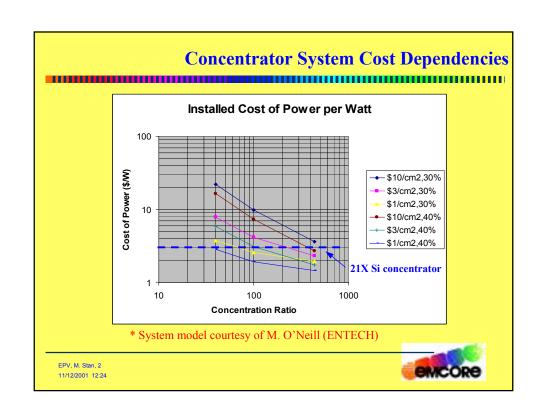
Slide 14 - High-Performance Kick-Off Meeting at NREL 10/18/01

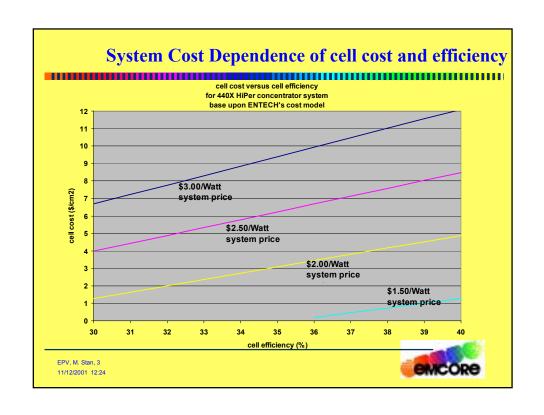
A 3J Solar Cell for High Concentration Terrestrial Applications

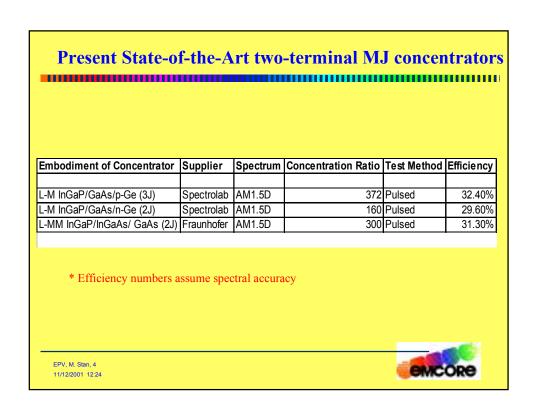
Mark A. Stan
Emcore Photovoltaics
10420 Research Road, SE
Albuquerque, NM 87123

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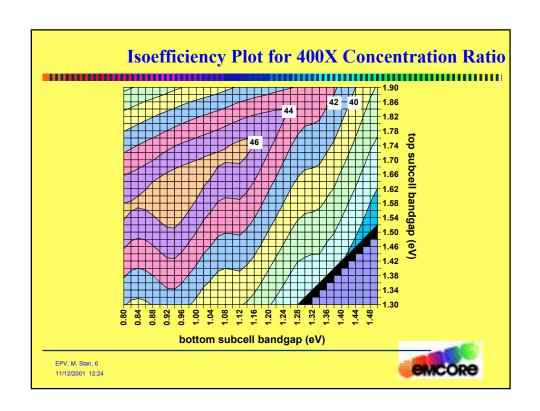


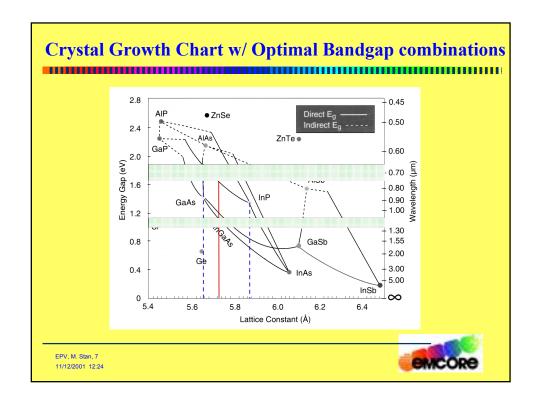
Technical Approach

- Target > 40% cell design for 400X-500X concentration.
- Leverage knowledge of 3J growth on Ge with Latticemismatched III-V materials technology presently used by Emcore for Thermophotovoltaic (TPV)devices.
- Device modeling to be performed by Dan Aiken (Emcore) & Prof. Jeffery Gray at Purdue University to determine optimum concentrator cell structure.
- On-sun receiver performance measurements will be made by Amonix.

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Anticipated Issues for Success of Lattice-Mismatched Cells

- Development of lattice constant grading buffer structure from Ge to In_xGa_(1-x)As (0<X<0.2)
 - must minimize threading dislocation density
 - Benchmarks on GaAs
 - » 21.6% (AM1.5G) n/p In_{0.17}Ga_{0.83}As (Fraunhofer)
 - » Voc=786 mV, Jsc=36.4 mA/cm², FF=79%
- Development of a high peak current density tunnel diode
 - 500X requires Jp=8 A/cm²
 - Fraunhofer AlGaAs/InGaP diode Jp=0.53 A/cm²
 - Spectrolab high Eg diode did not current limit at 500X

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Anticipated Issues for Success of Lattice-Mismatched Cells

■ Lattice-matching of top cell to middle cell

- Tensile strain can cause severe wafer bowing with 140um thick Ge used for space solar applications.
- High resolution asymmetric bragg x-ray diffraction is necessary for proper matching of solar cell active layers.
- Reproducibility of InGaAs misfit dislocation network
- Impact of misfit/threading dislocations on In_xGa_{1-x}P bulk properties such as minority carrier lifetime is unknown.

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Project Milestone Chart (Months after start of program) 9 10 11 12 13 14 15 16 17 Year 1 1. Performance Modeling 2. Mask Design 3. Buffer Layer 4. InGaAs Subcell 5. Tunnel Diodes 1. InGaP Subcell 2. Device Integration 3. AR Coating 4. Contact Metallization 5. Concentrator System Final Report Monthly Technical & **Financial Reports** NREL Annual Review FPV. M. Stan. 10 11/12/2001 12:24

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The High Performance Photovoltaic Project held a Kickoff Meeting in October, 2001. This booklet contains the presentations given by subcontractors and in-house teams at that meeting. The areas of subcontracted research under the HiPer project include Polycrystalline Thin Films and Multijunction Concentrators. The in-house teams in this initiative will focus on three areas: 1.) High-Performance Thin-Film Team-leads the investigation of tandem structures and low-flux concentrators, 2.) High-Efficiency Concepts and Concentrators Team-an expansion of an existing team that leads the development of high-flux concentrators, and 3.) Thin-Film Process Integration Team-will perform fundamental process and characterization research, to resolve the complex issues of making thin-film multijunction devices.			
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